

Dynamic Information Flow Tracking on hardcore CPU

Problem statement

Ensure the following properties during execution on hardcore CPU

- information **confidentiality**
- information **integrity**

Current solutions

Software, Hardware (FPGA) and Hybrid DIFT

Our approach: HardBlare

- Non-modified hardcore CPU (Zynq SoC, ARM Cortex-A9 + FPGA)
- Hardware-assisted DIFT system



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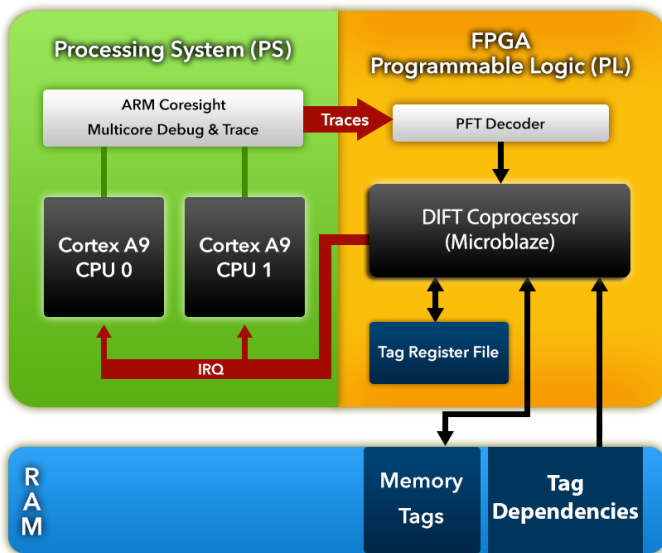
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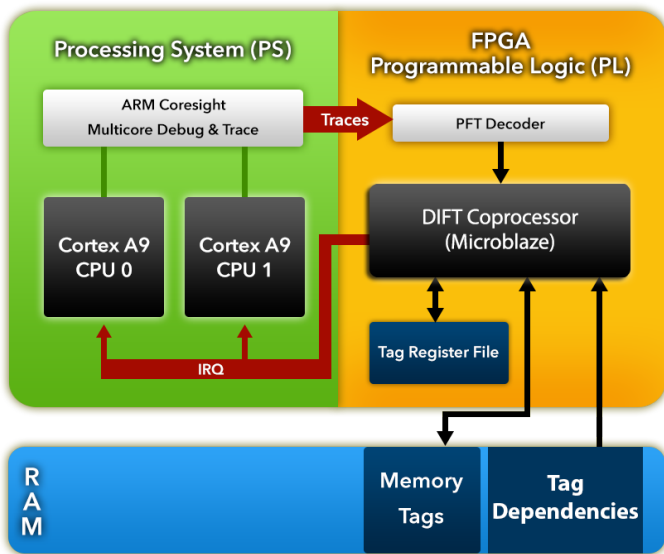
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Thank you and See you tomorrow !



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