



Boosting Convergence of Timing Closure using Feature Selection in a Learning-driven Approach

Que Yanghua, Harnhua Ng, Nachiket Kapre
yanghua.que@ntu.edu.sg, nachiket@ieee.org

Claim

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- Feature Selection helps boost AUC scores for Timing Closure ML models by ~10%

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- ML models predict timing closure of design by modifying CAD tool parameters — commercial tool InTime, by Plunify Inc.

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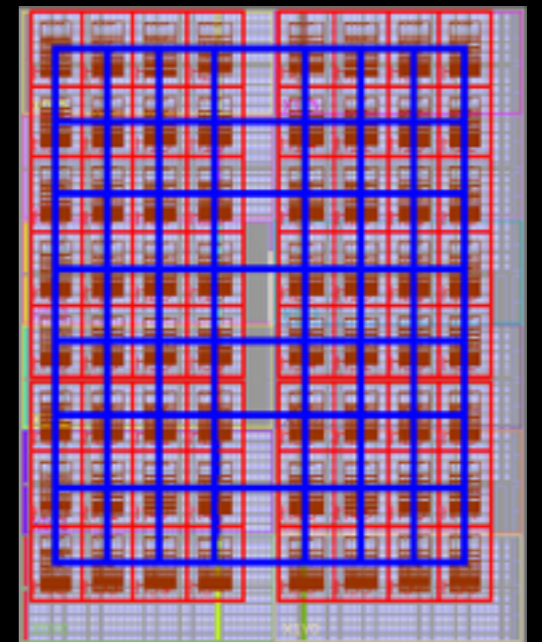
- Feature Selection helps boost AUC scores for Timing Closure ML models by ~10%
- ML models predict timing closure of design by modifying CAD tool parameters — commercial tool InTime, by Plunify Inc.
- For Altera Quartus
— ~80 parameters to 8-22 influential parameters

FPGA CAD Flow

Verilog/VHDL
Code

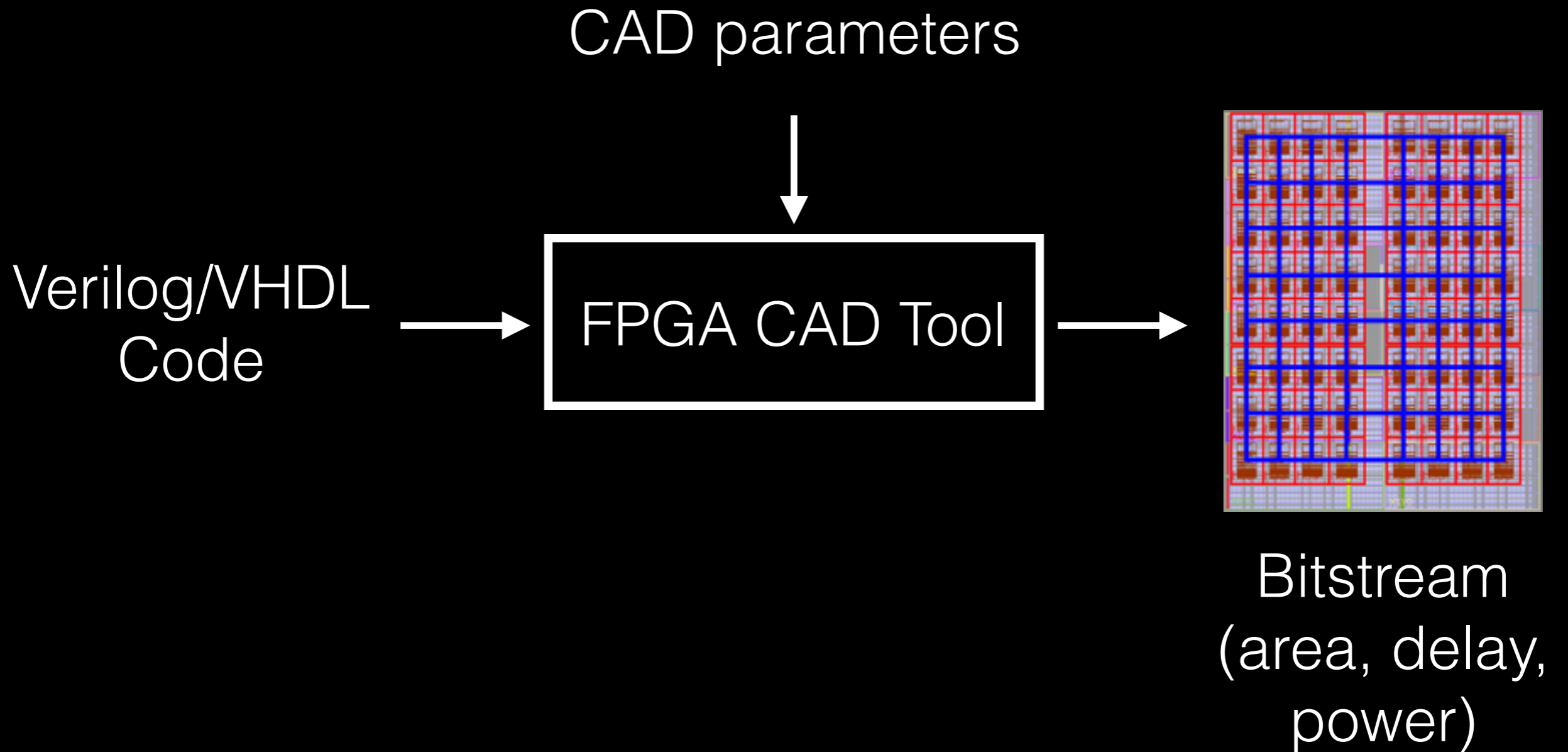


FPGA CAD Tool



Bitstream
(area, delay,
power)

FPGA CAD Flow

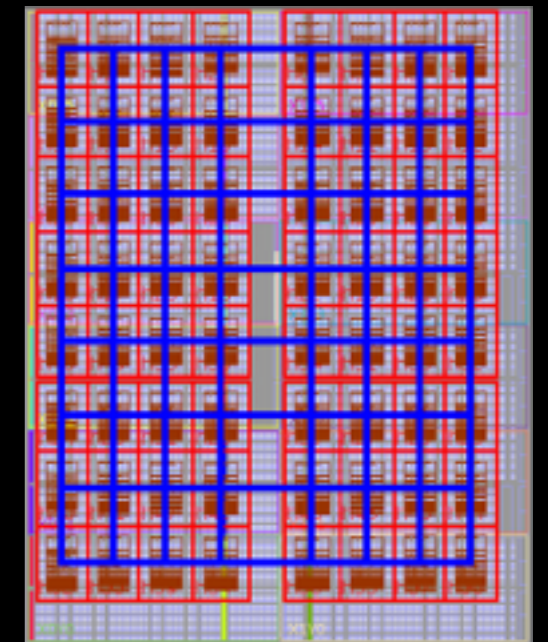


FPGA CAD Flow

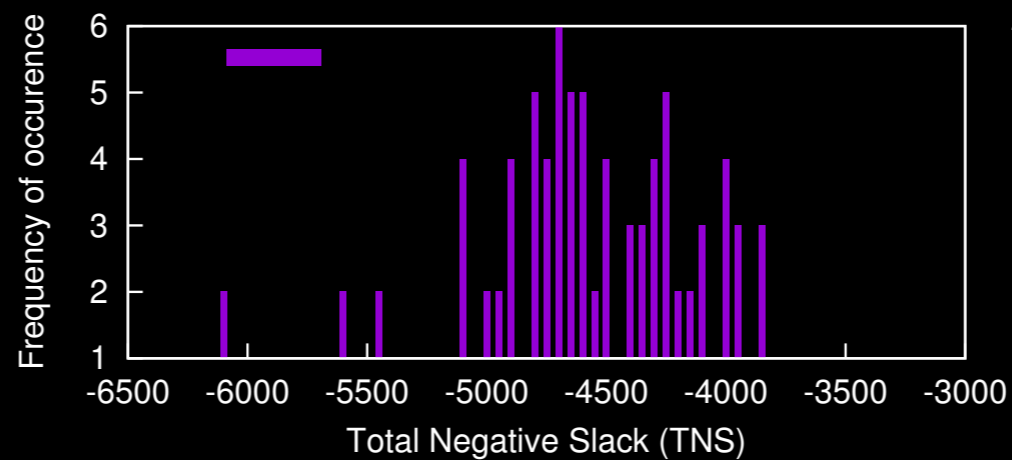
CAD parameters

Verilog/VHDL
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FPGA CAD Tool

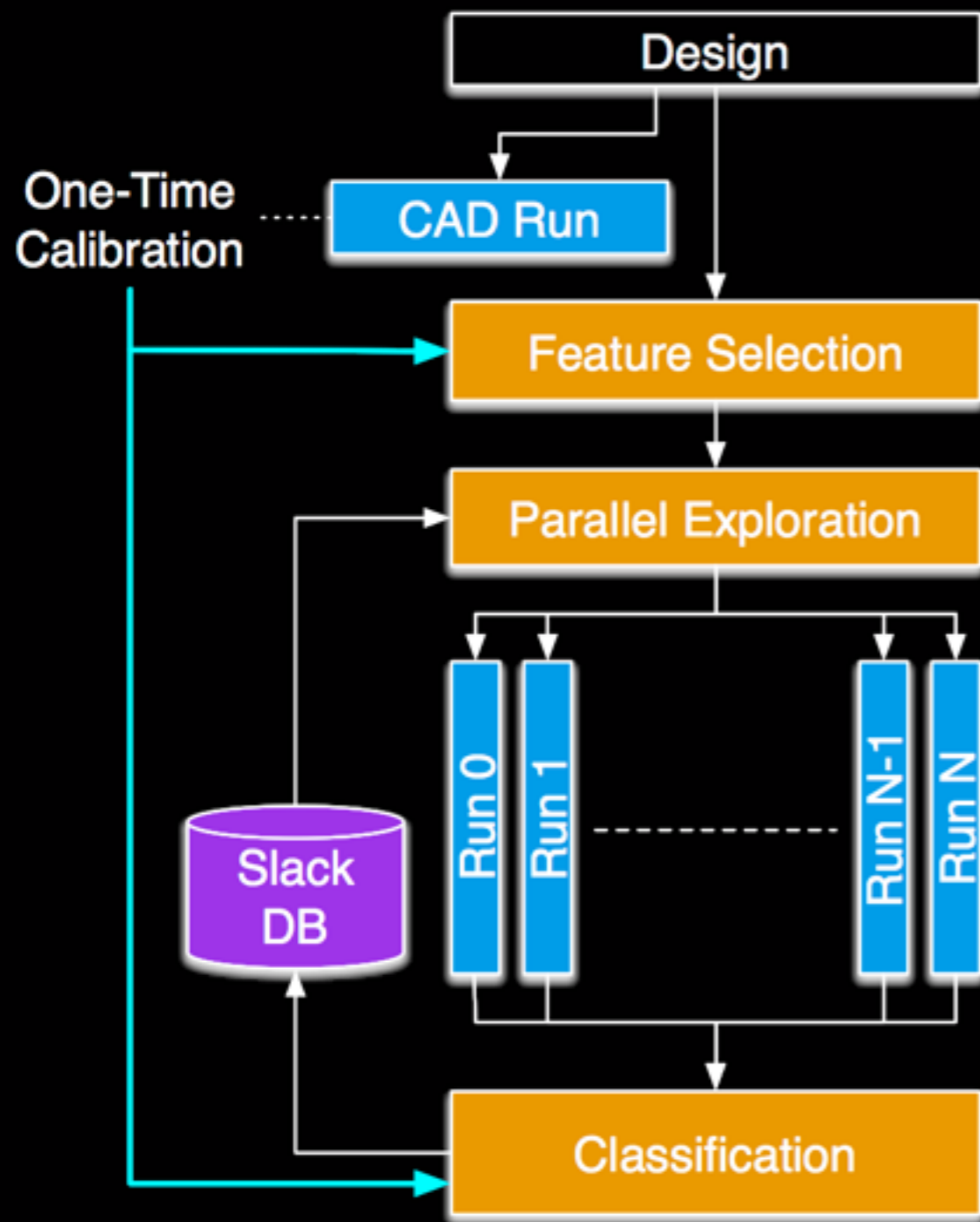


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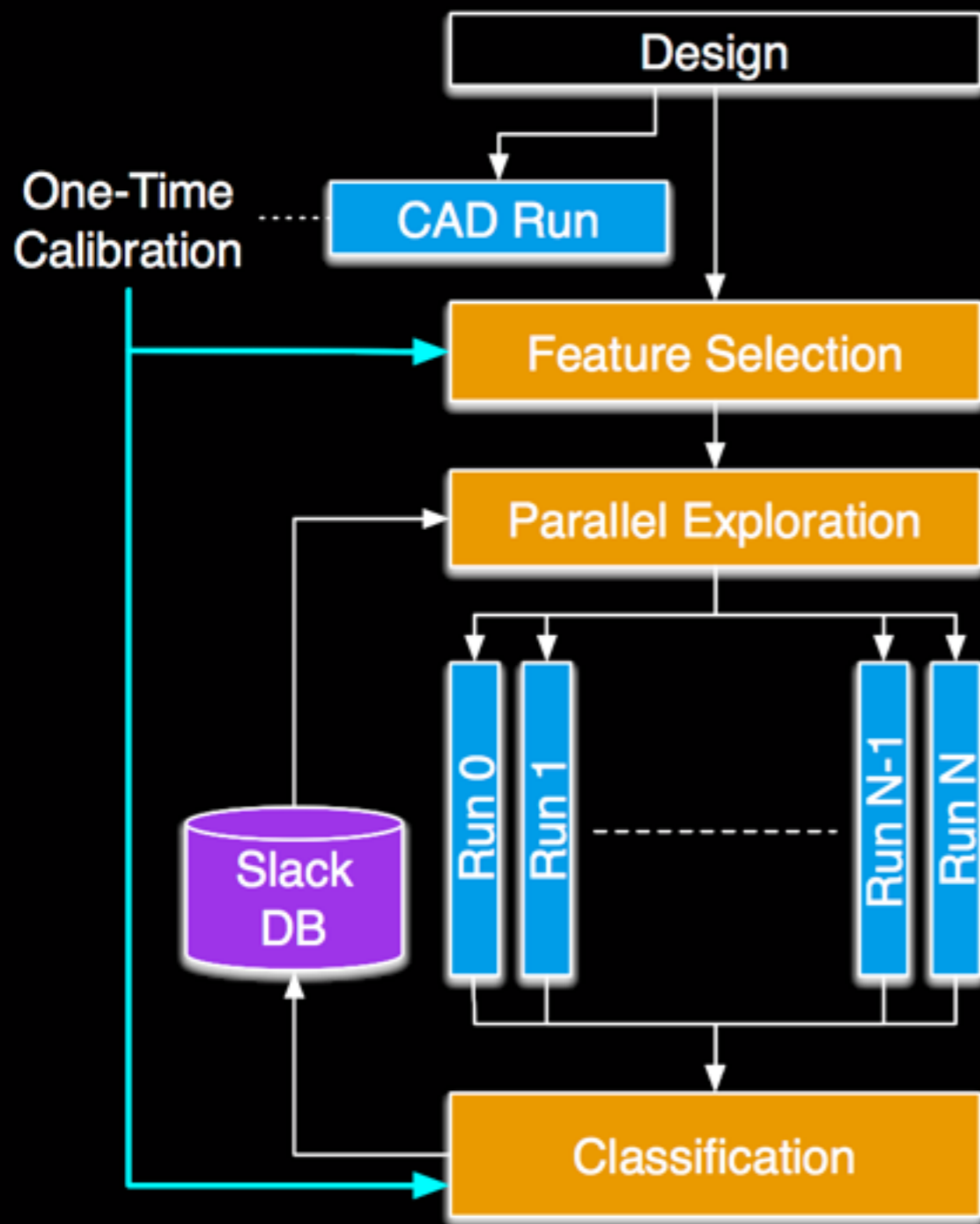


InTime High-Level View

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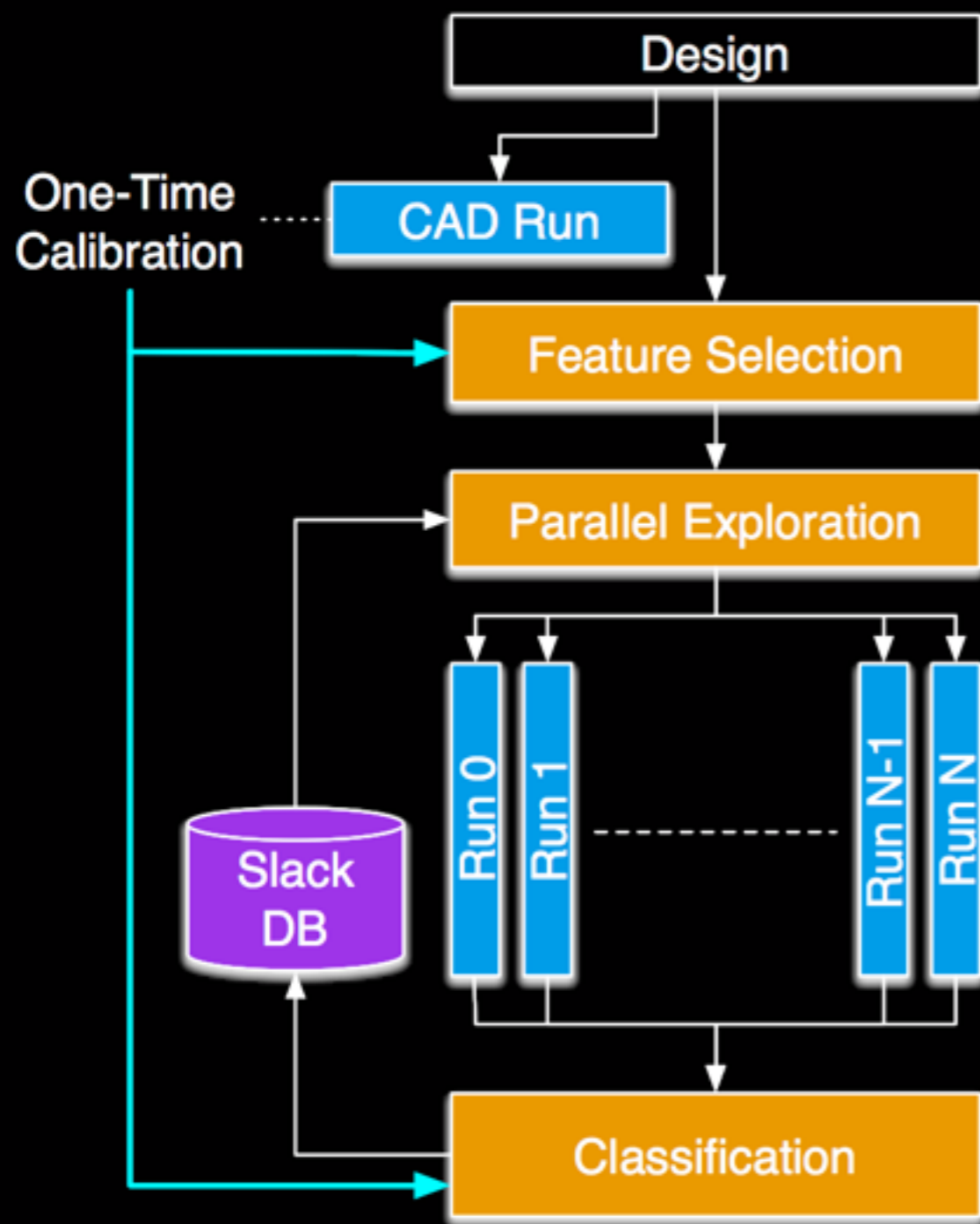


InTime High-Level View



- **Position:**
 - Verified RTL designs expensive to edit
 - For timing closure, use CAD parameters

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 - free RTL, play with CAD tool parameters
 - **Problem:** exhaustive search intractable
 - **Solution:** use machine learning!

InTime High-Level View

[FPGA'15 Designer's Day] Preliminary results on customer designs (limited ability to discuss specifics)

[FCCM'15 Full] Extended results quantifying ML effects on open-source benchmarks

[FPGA'16 Short] Case-for “design-specific” learning instead of building a generic model

[FCCM'16 Short] Classifier accuracy exploration across ML strategies, and hyper-parameter tuning

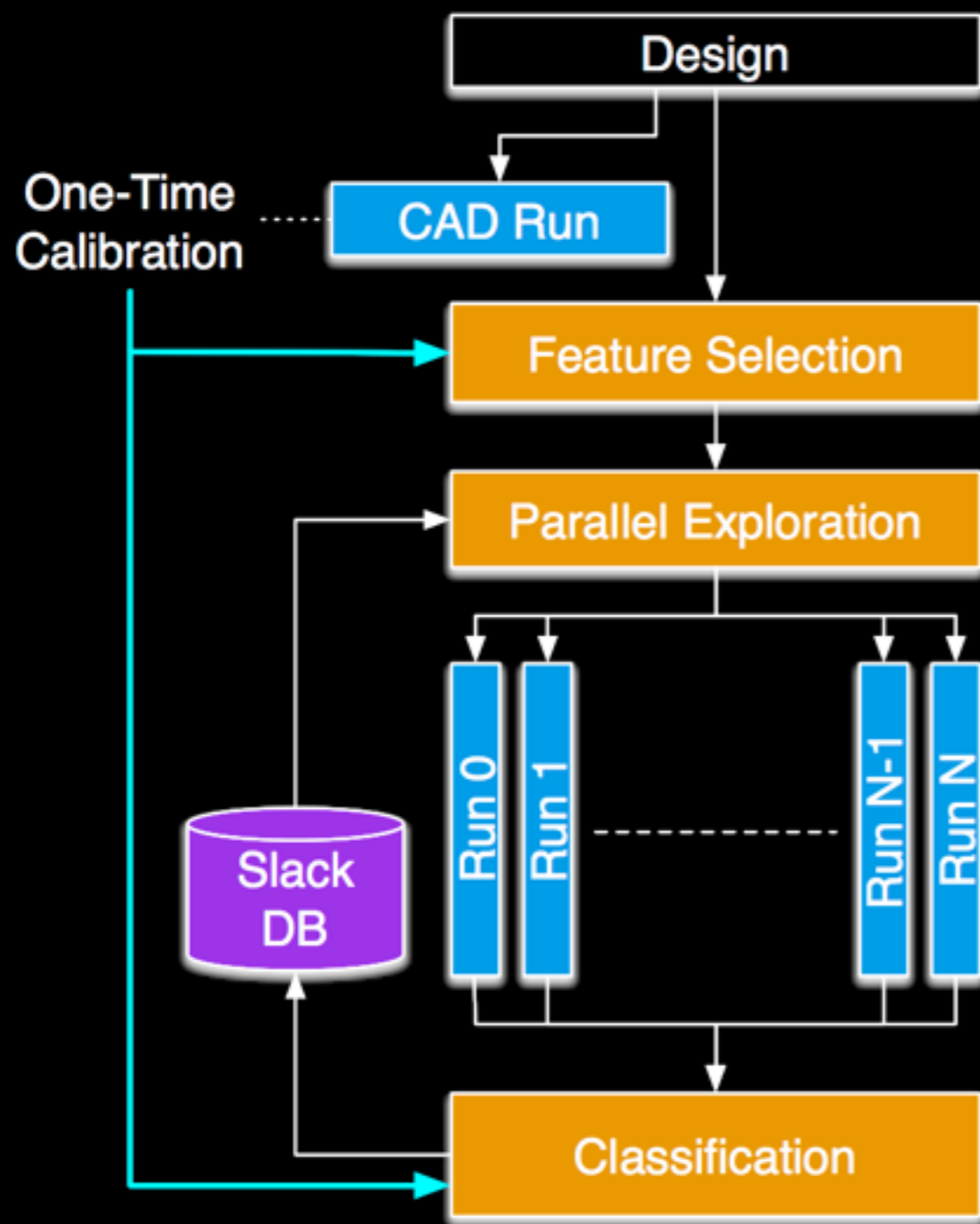
Outline

- Brief intro of InTime flow and ML techniques
- Justifying the approach
 - Opportunity for using ML (Slack distribution)
 - The need for running ML (Entropy/Correlation)
- Review of Feature Selection
- Experimental results
 - Impact of features/run samples
 - ROC curves across designs
 - Comparing vs. FCCM'16 results

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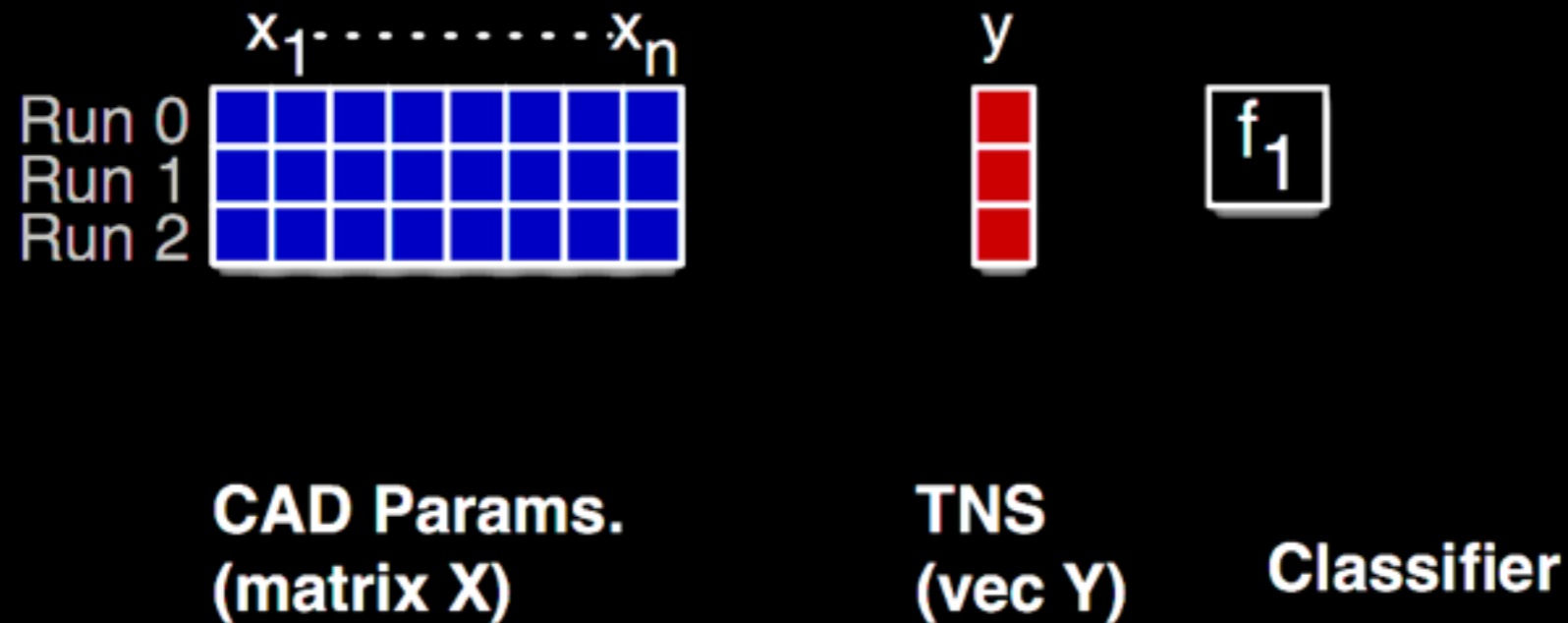
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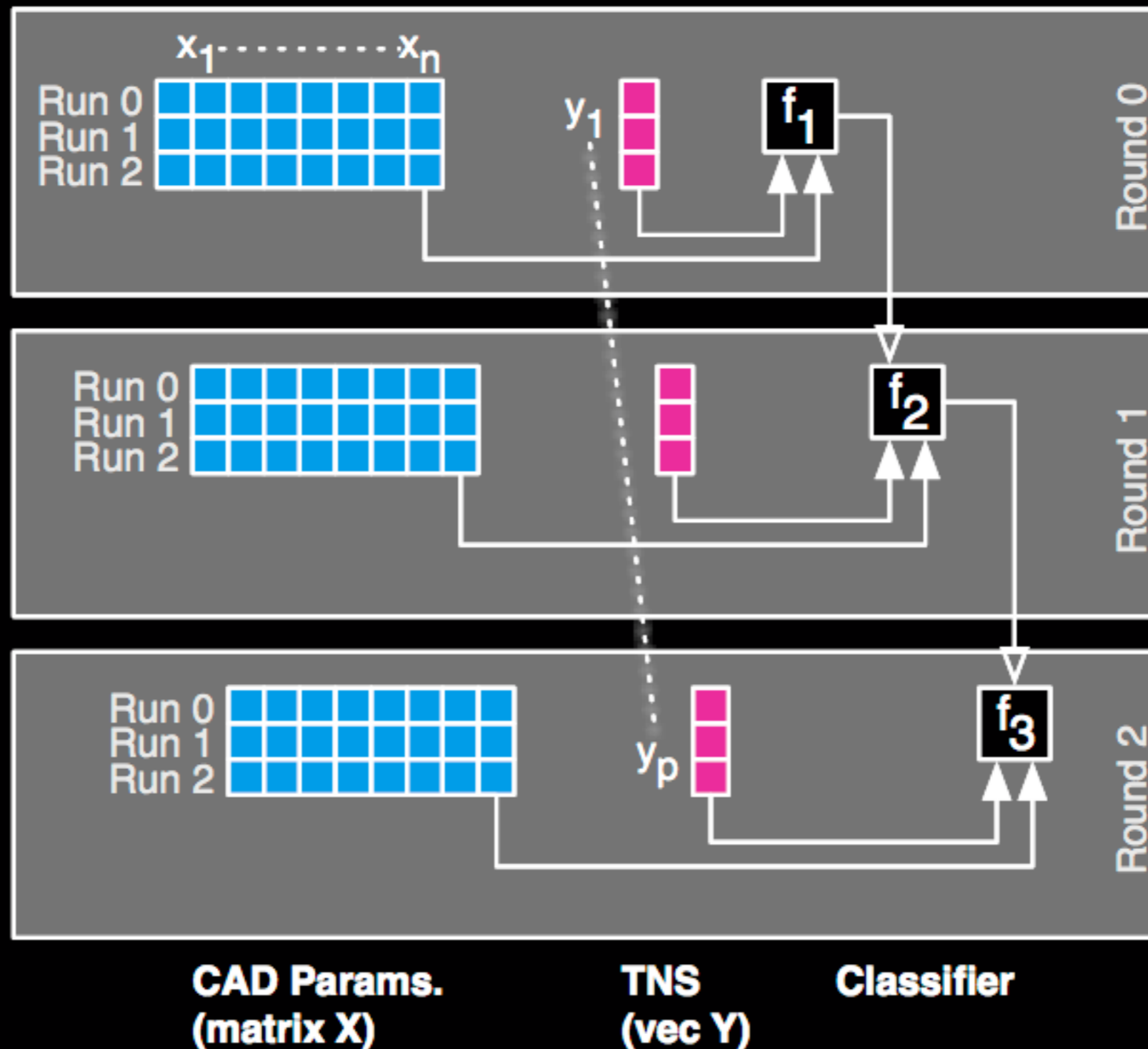
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How InTime works



- Simply tabulate results
 - record input CAD parameters + timing slack
- Build a model for predicting [GOOD/BAD]

How InTime works



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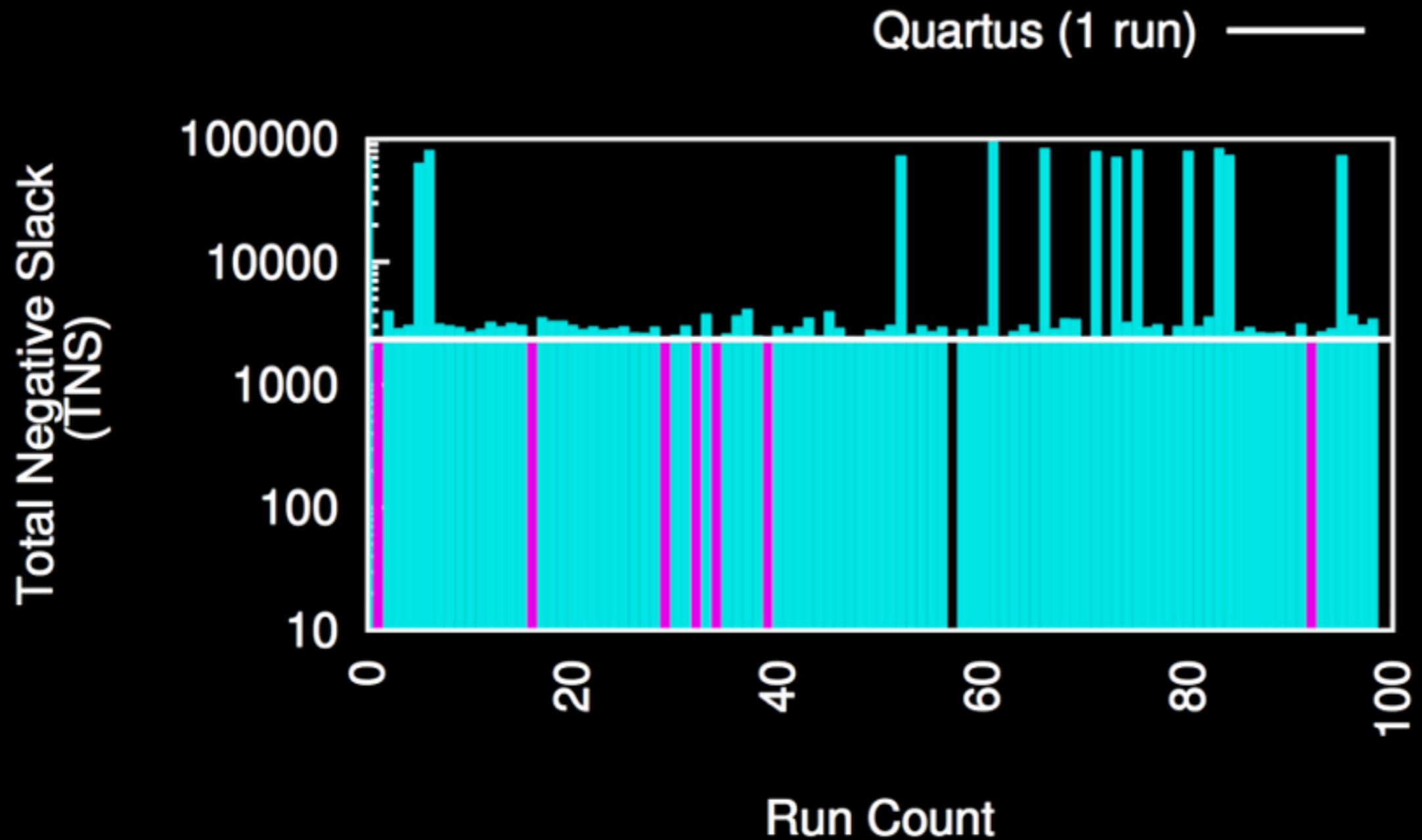
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Q&A

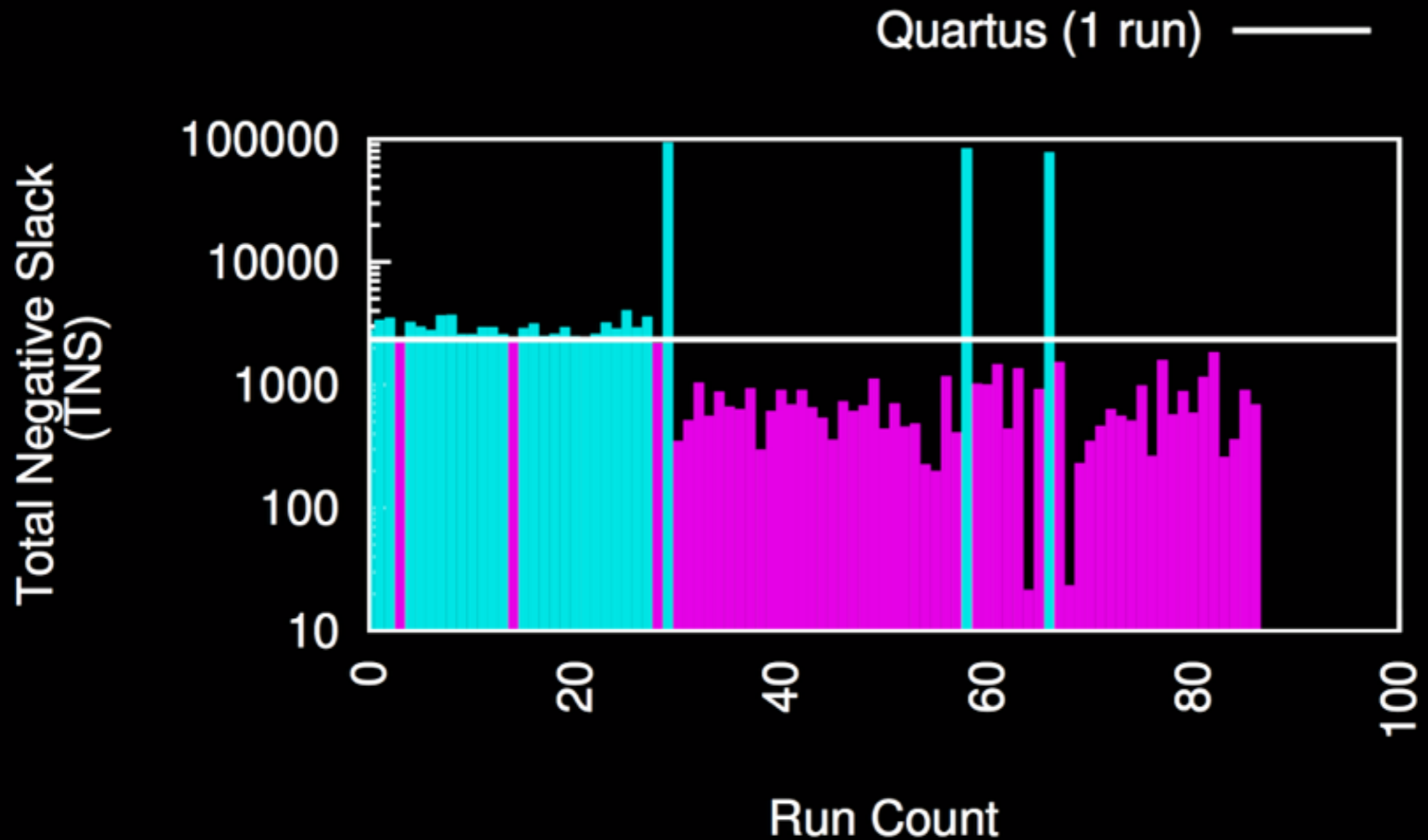
- Do this really work?
- What's the opportunity in timing slack spread?
- Do we really need machine learning?
- How unique are the final converged solutions?
- What is the coverage scope of our tool?

Do this really work?

Results — No Learning

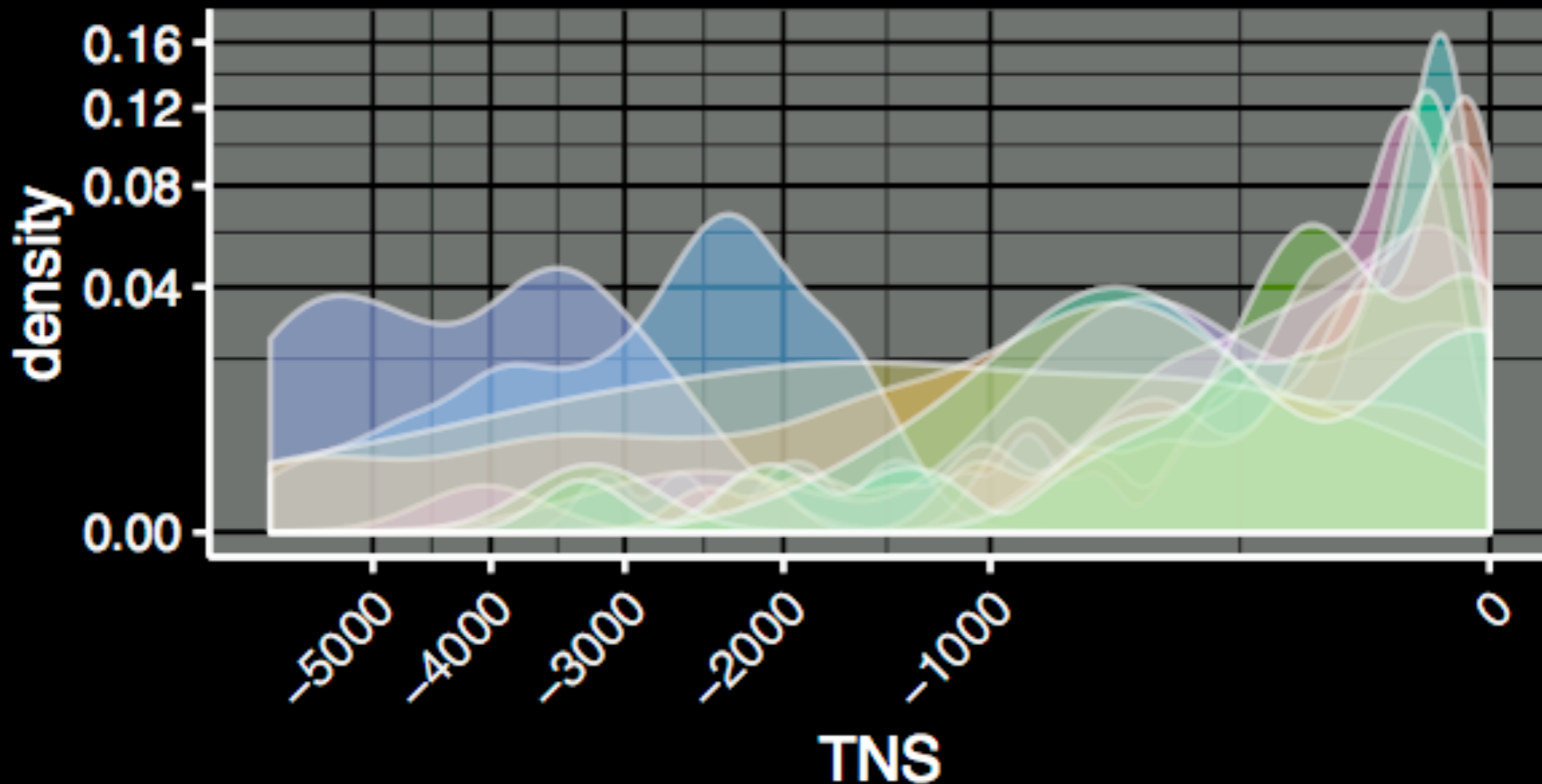
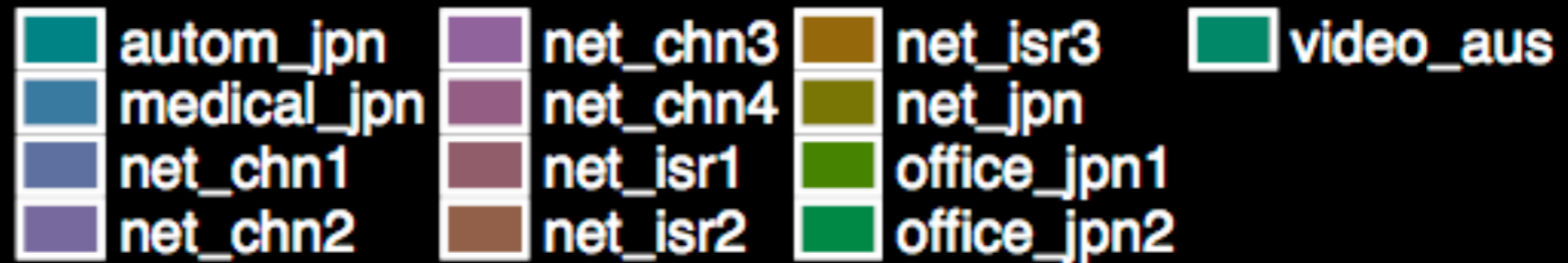


Results — with Learning



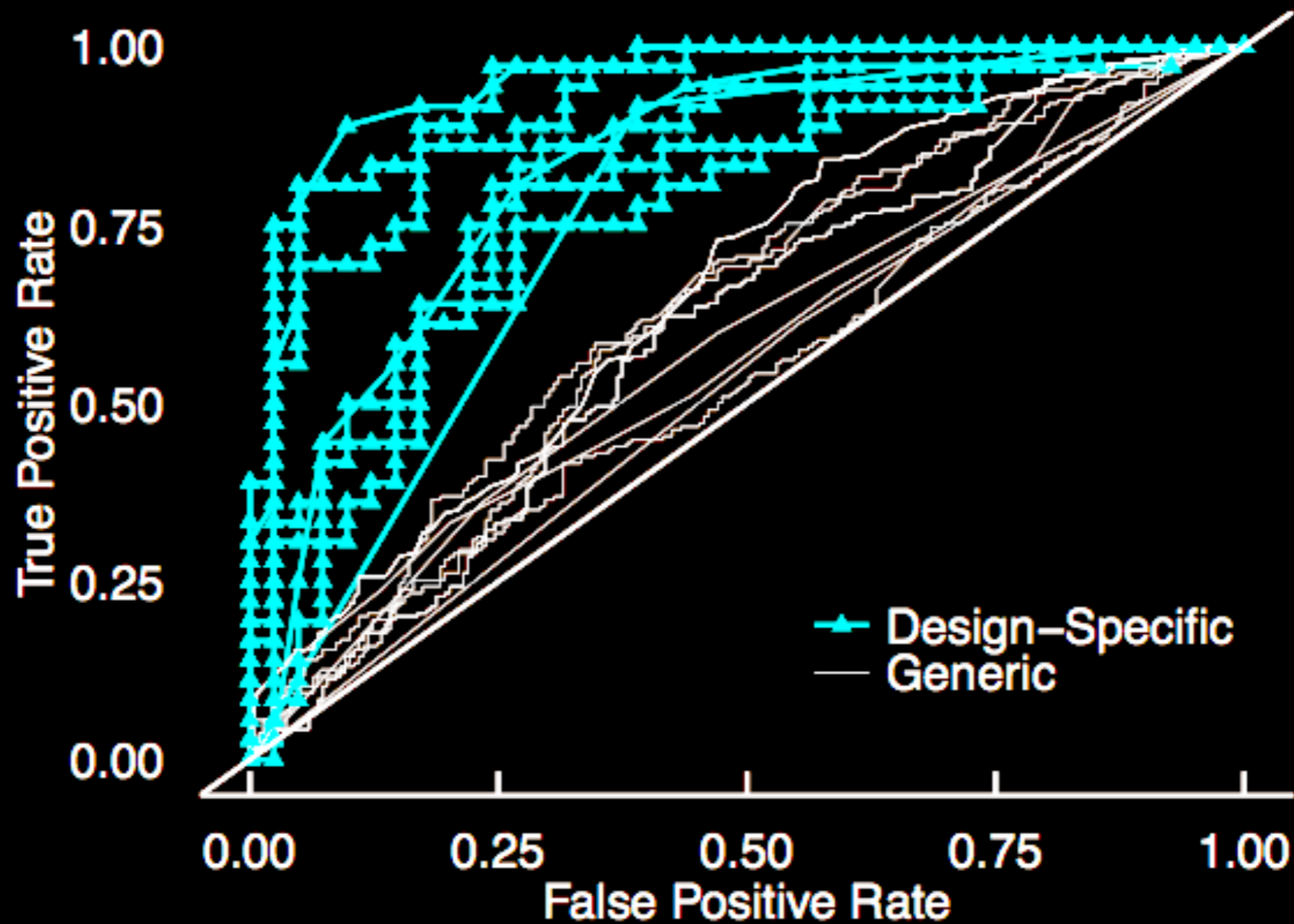
What's the opportunity
in timing slack spread?

Parameter Exploration



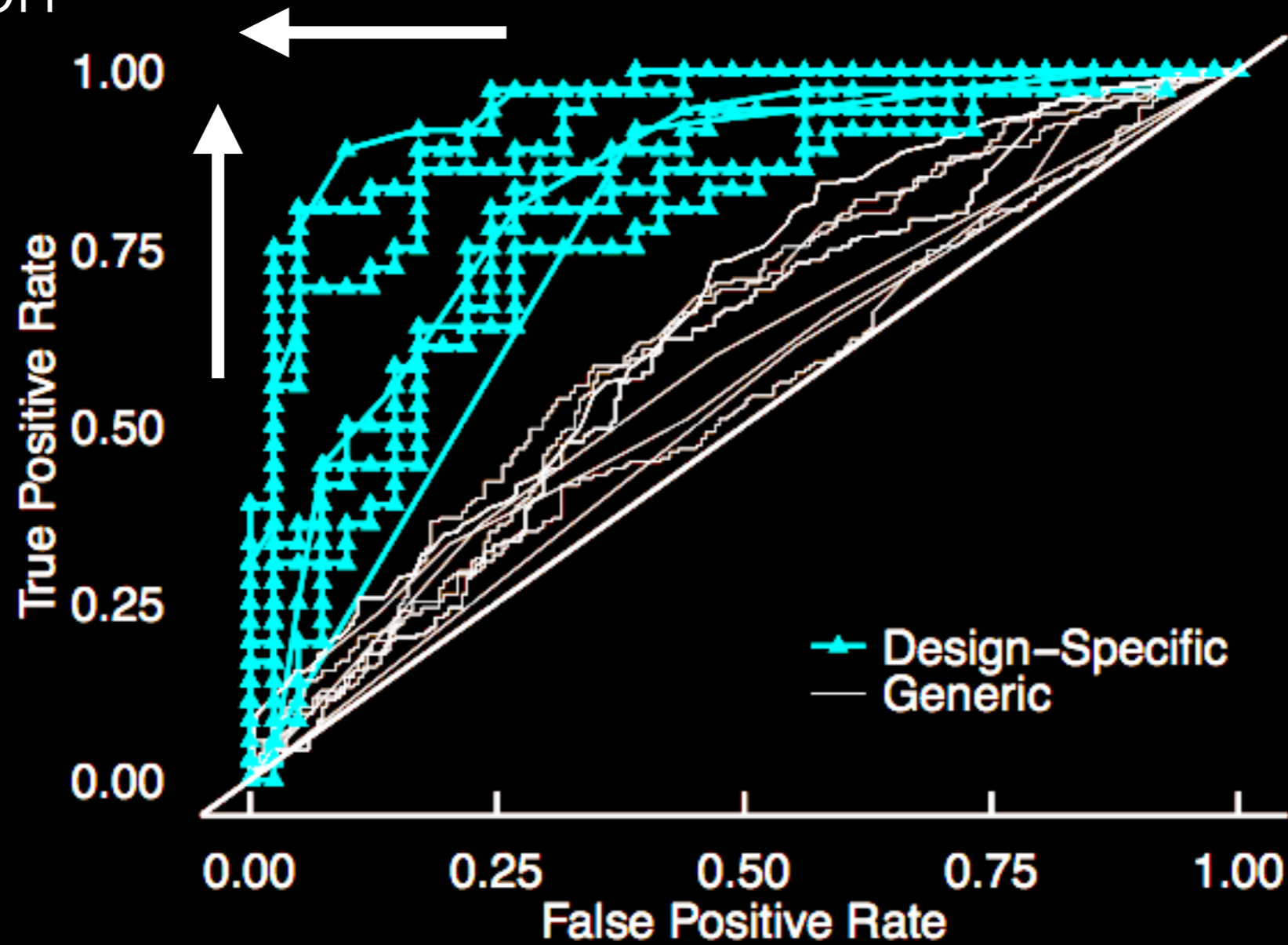
Do we really need
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Results (aes)



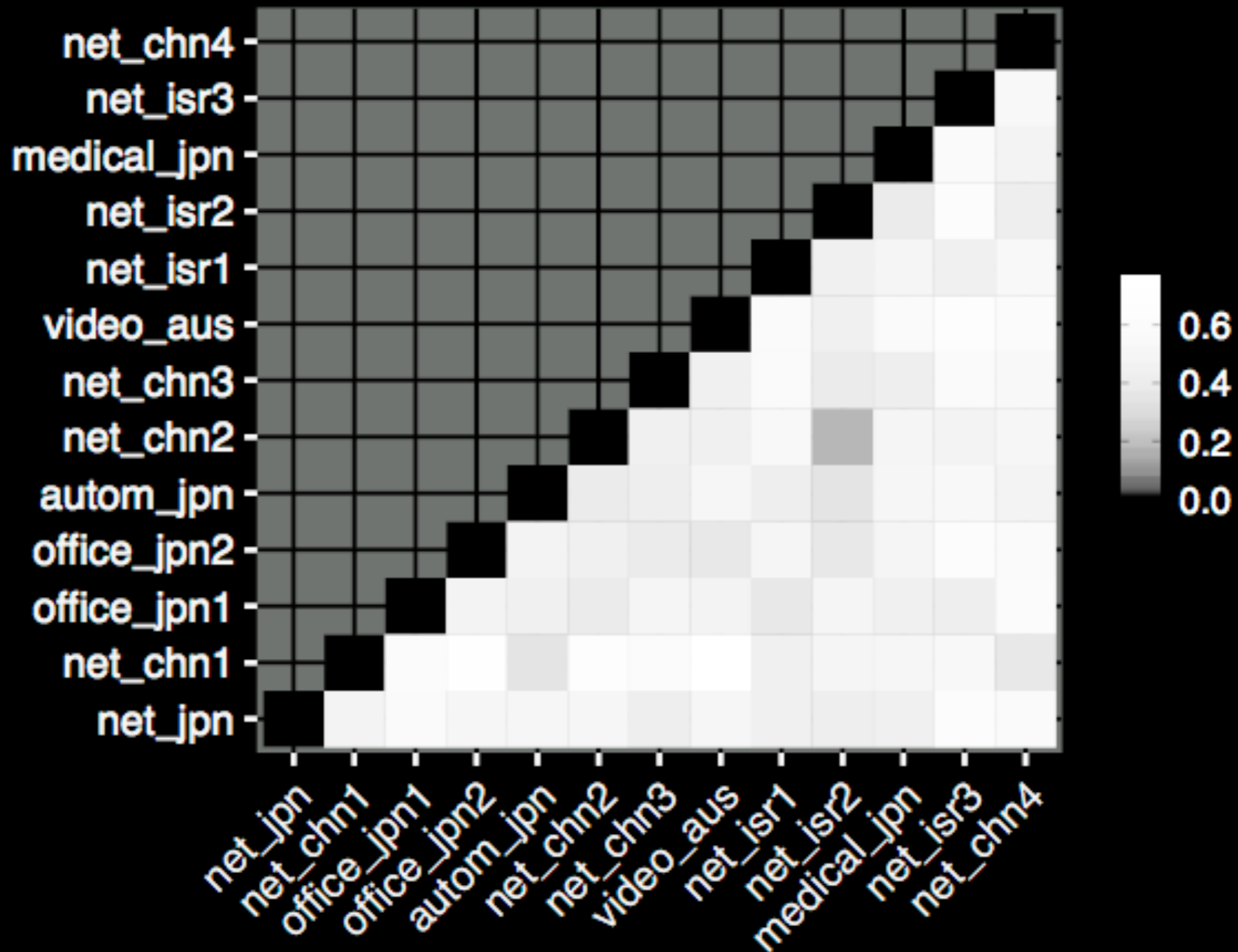
Results (aes)

best
classification



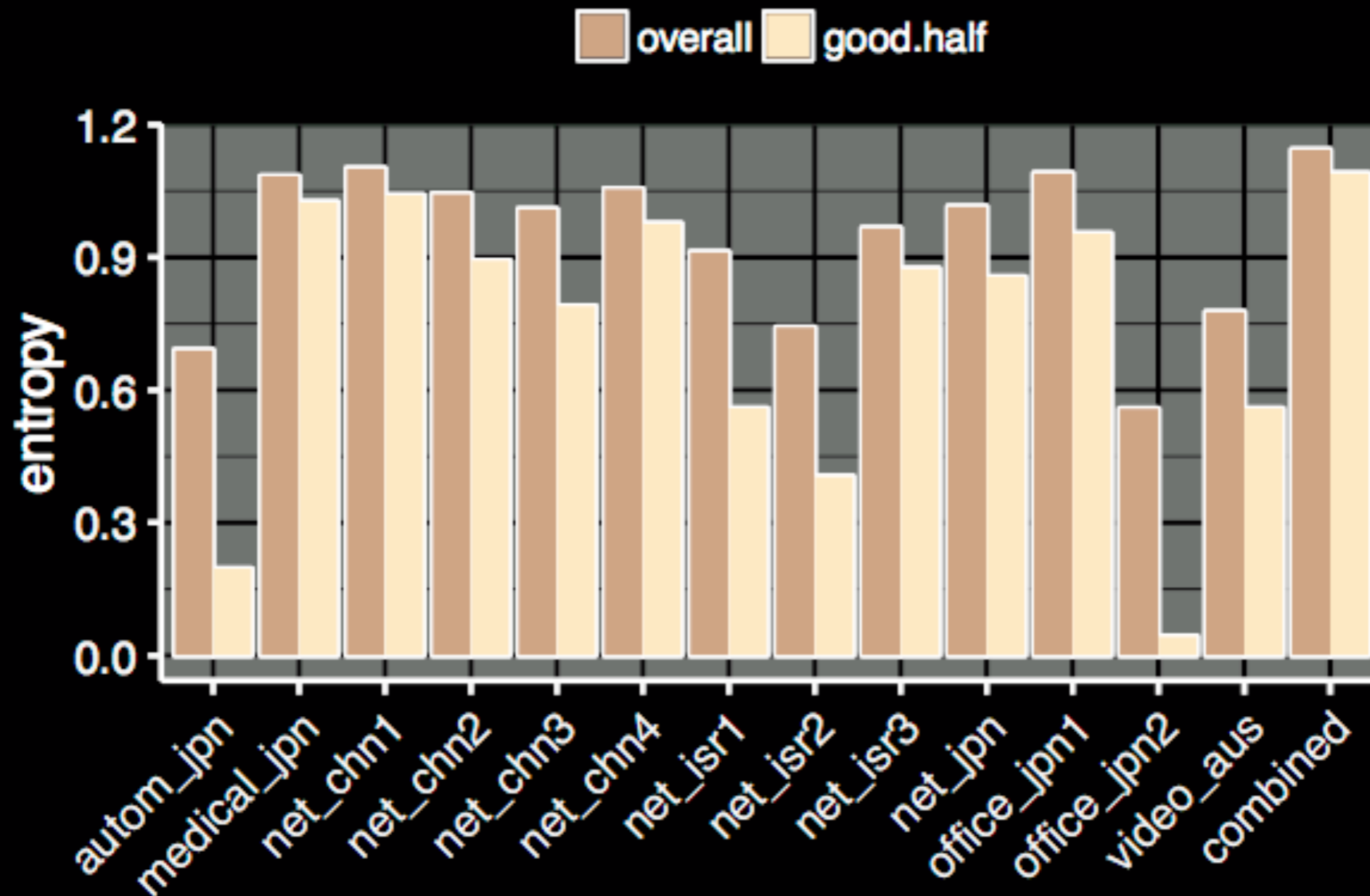
How unique are the final converged solutions?

Dissimilarity



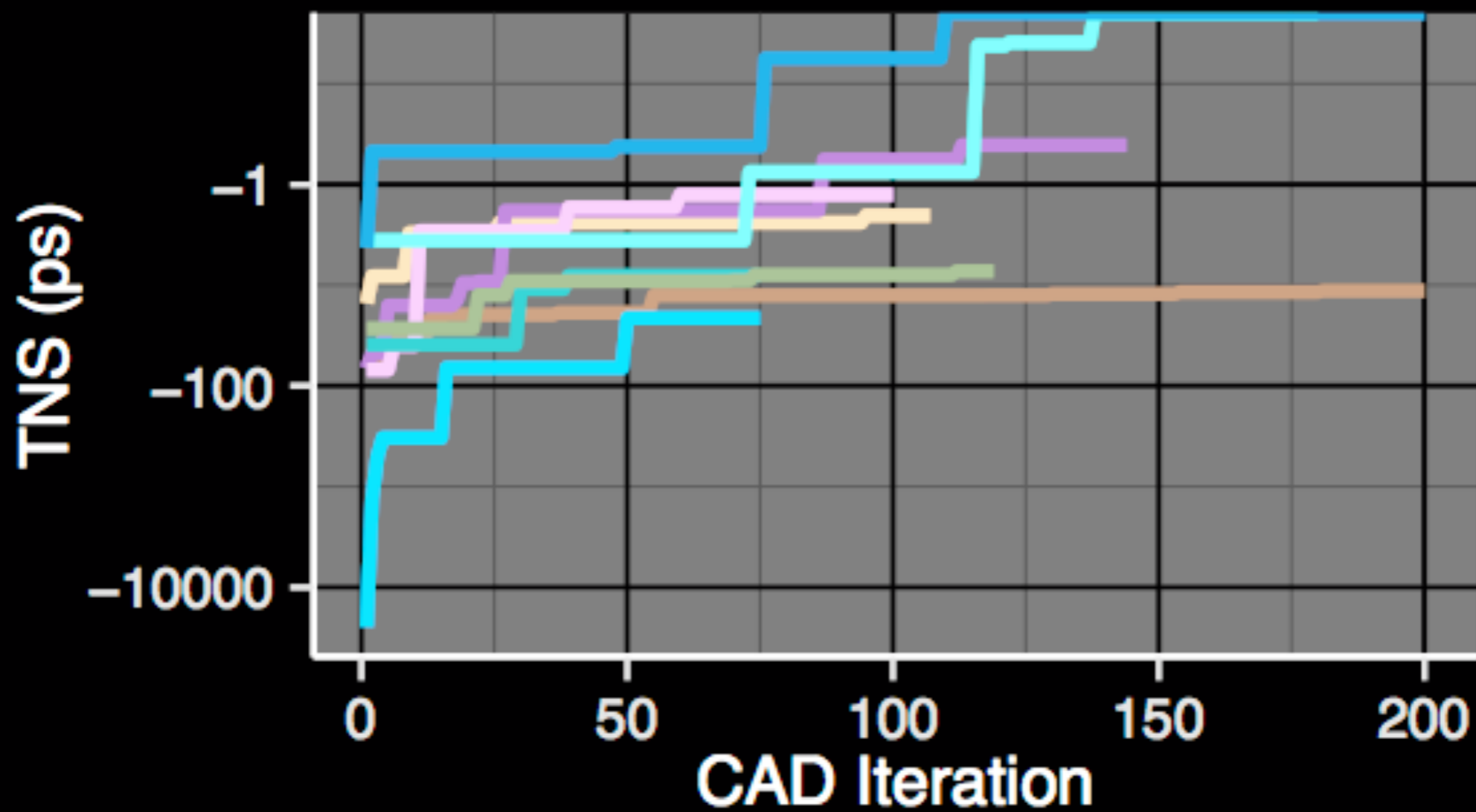
What is the coverage
scope of our tool?

Entropy in solutions



So, what's the
bottomline?

— SOC — autom_jpn — net_chn4 — net_isr2 — office
— VIP — net_chn3 — net_isr1 — net_isr3

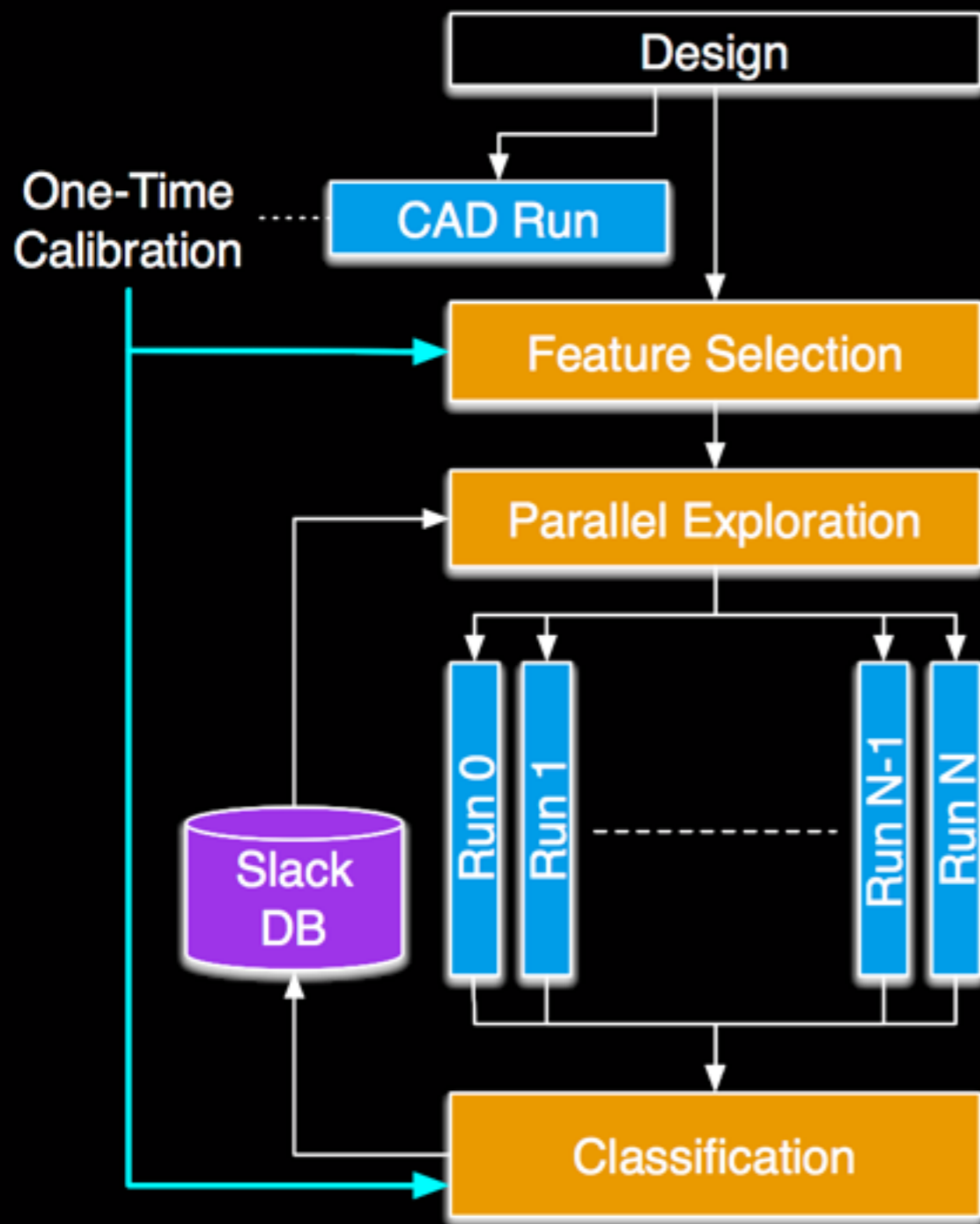


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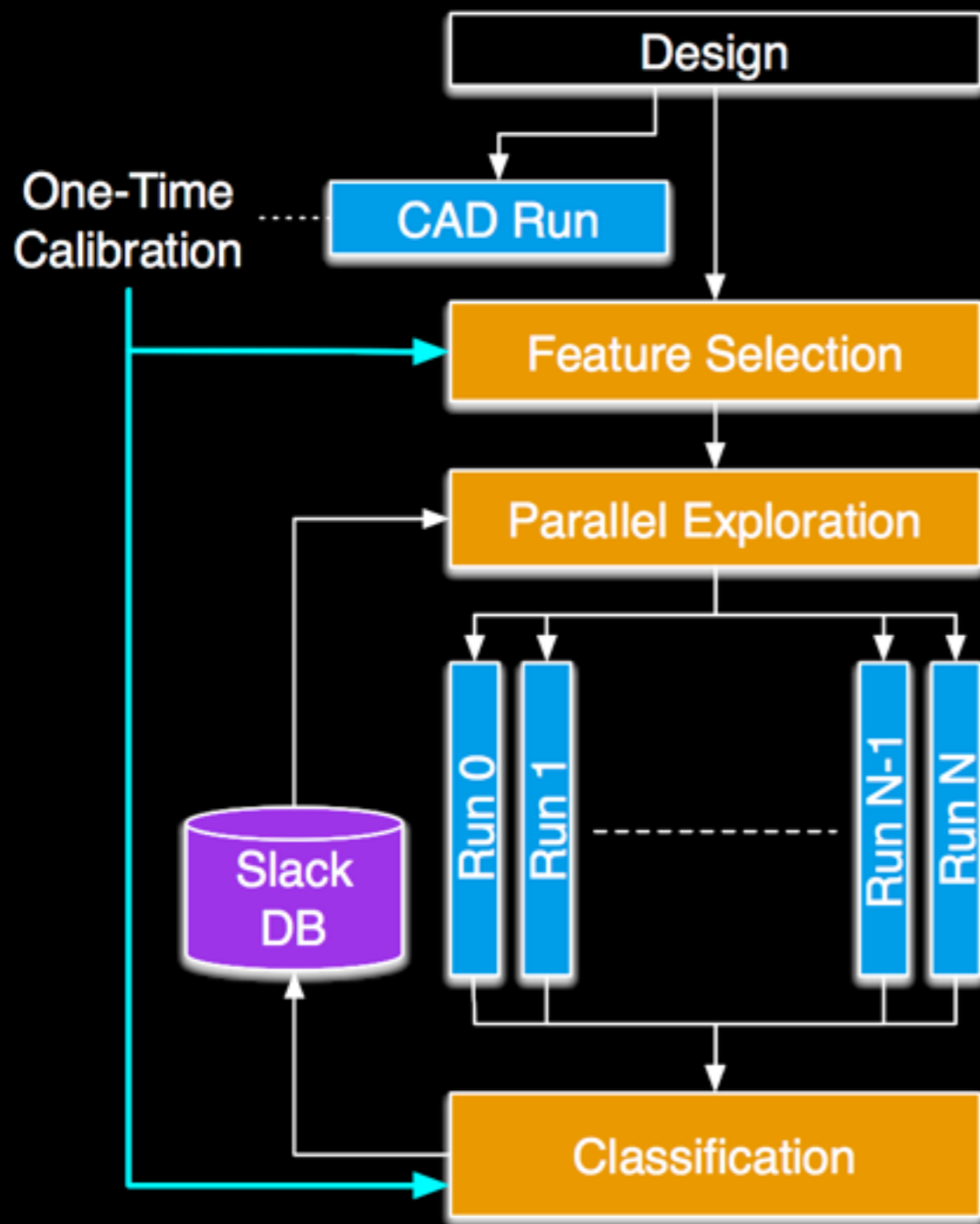
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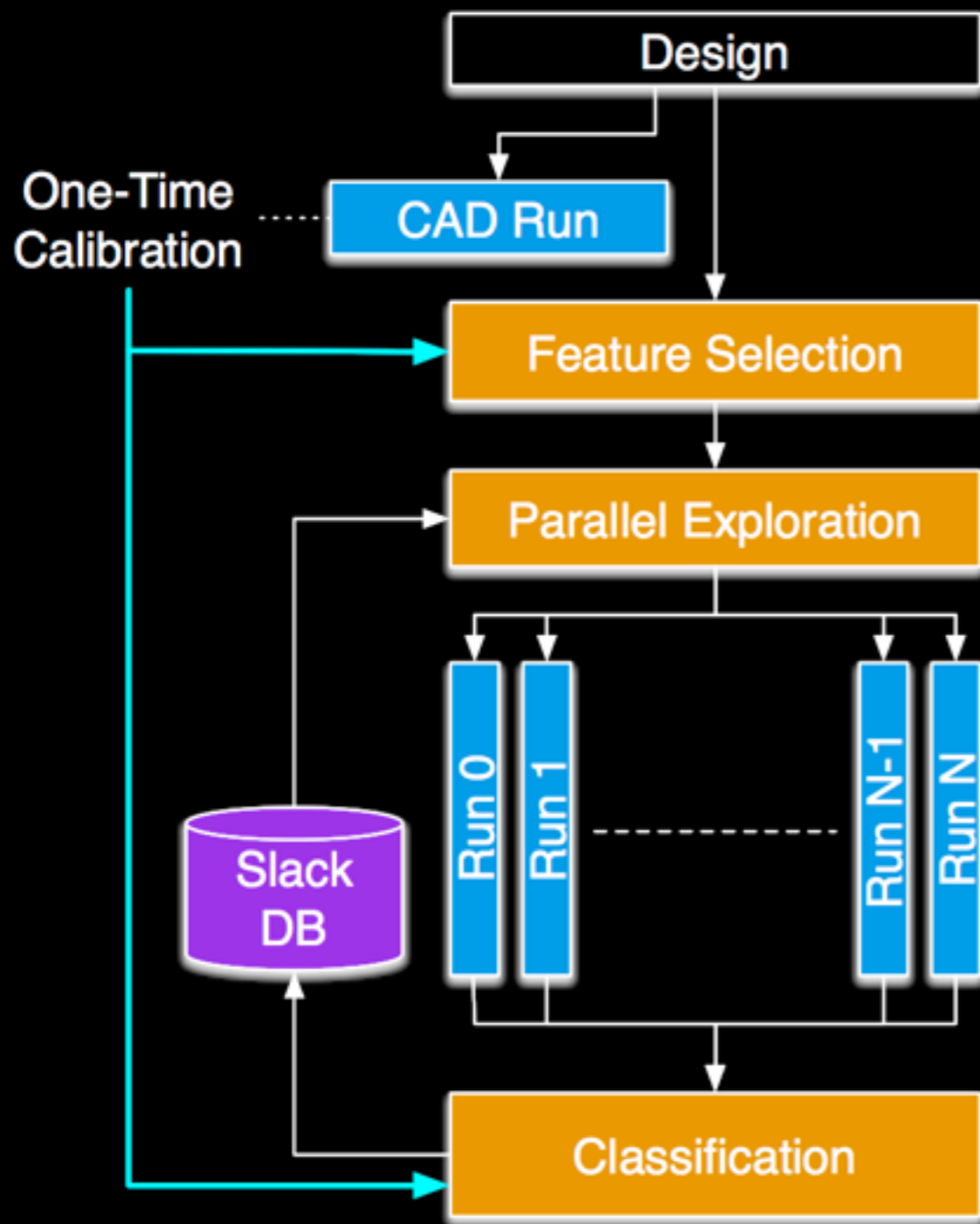


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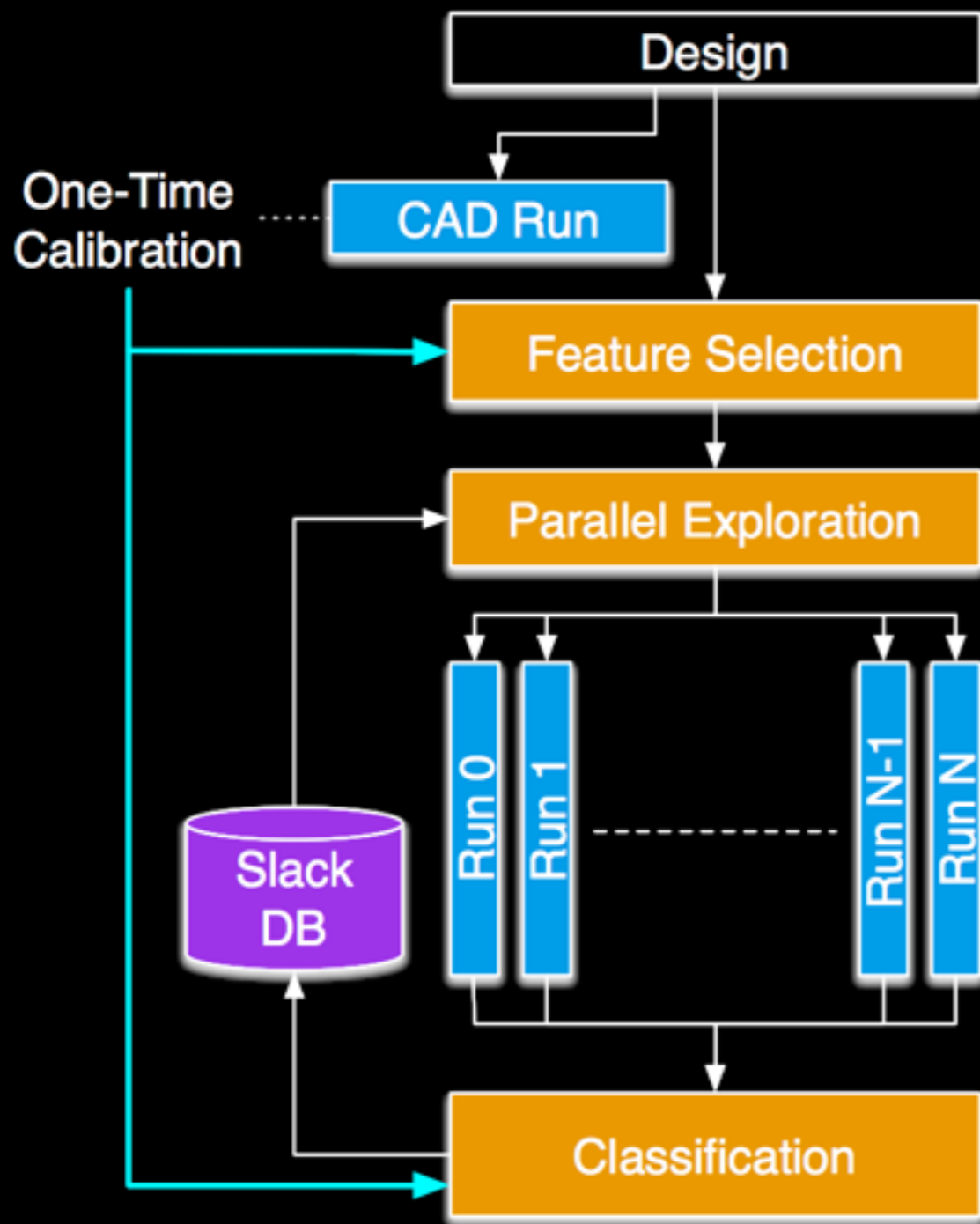
- **Hypothesis:** Not all CAD parameters affect timing outcome

Feature Selection



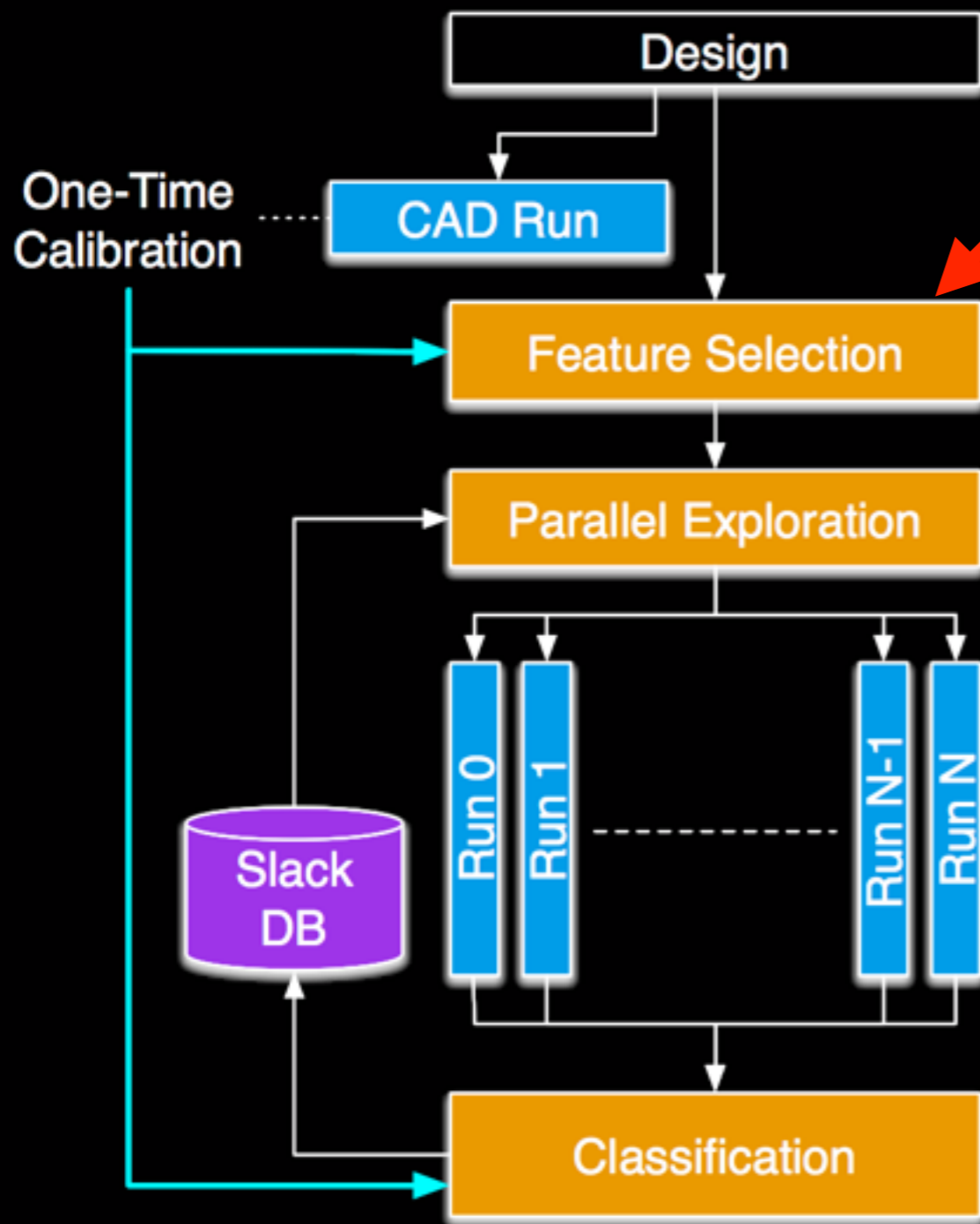
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- Can we find the most relevant parameters?

Feature Selection



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- Feature selection: known technique in ML circles
 - avoid noise during classification
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Techniques

- OneR — use frequency of class labels
- Information.Gain — uses entropy measure
- Relief — clustering of parameters
- Ensemble — combination of above...

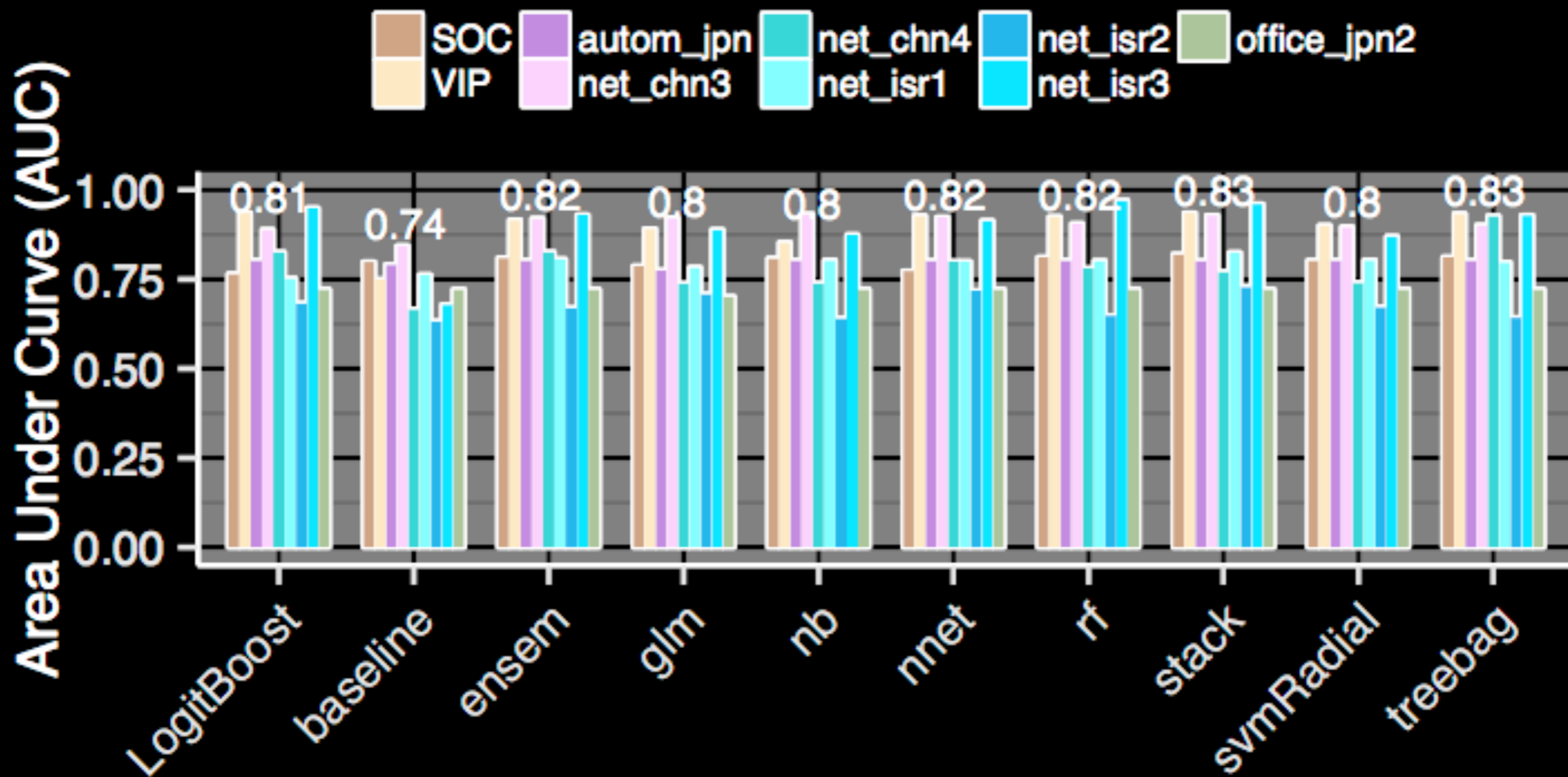
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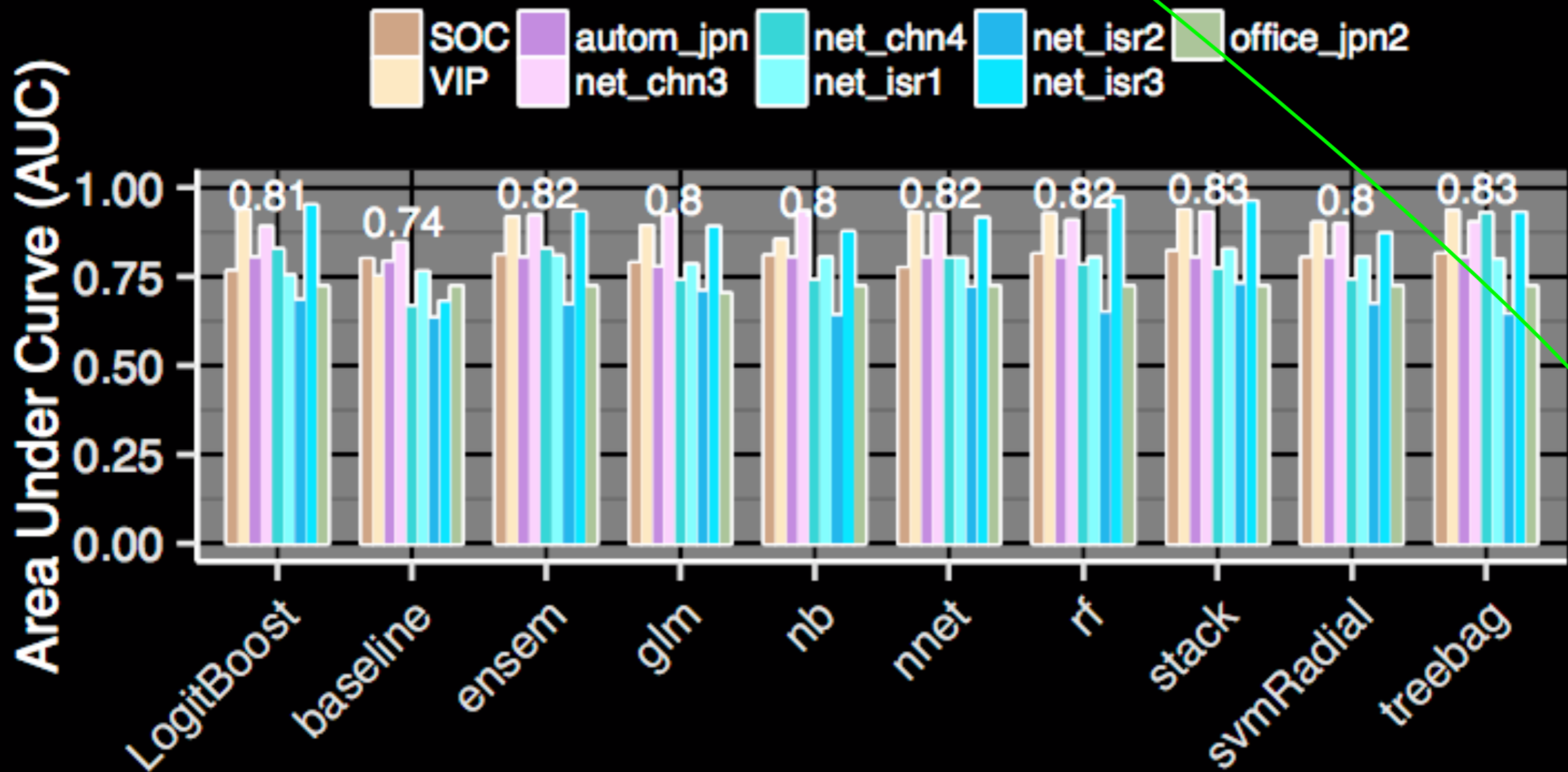
Q&A

- How effective is feature selection?
- How long does the learning process take?
- What is the impact of choosing feature count?

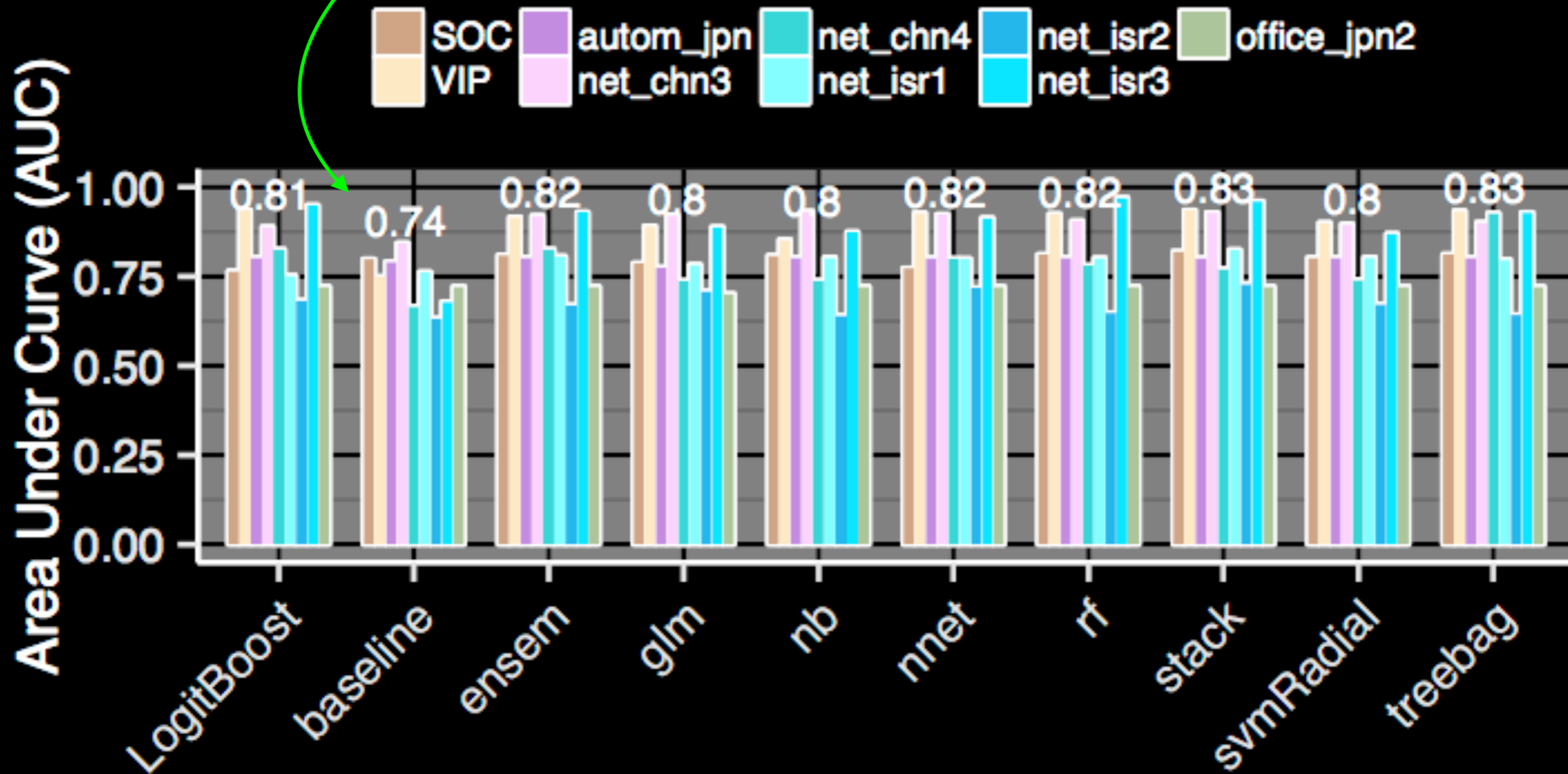
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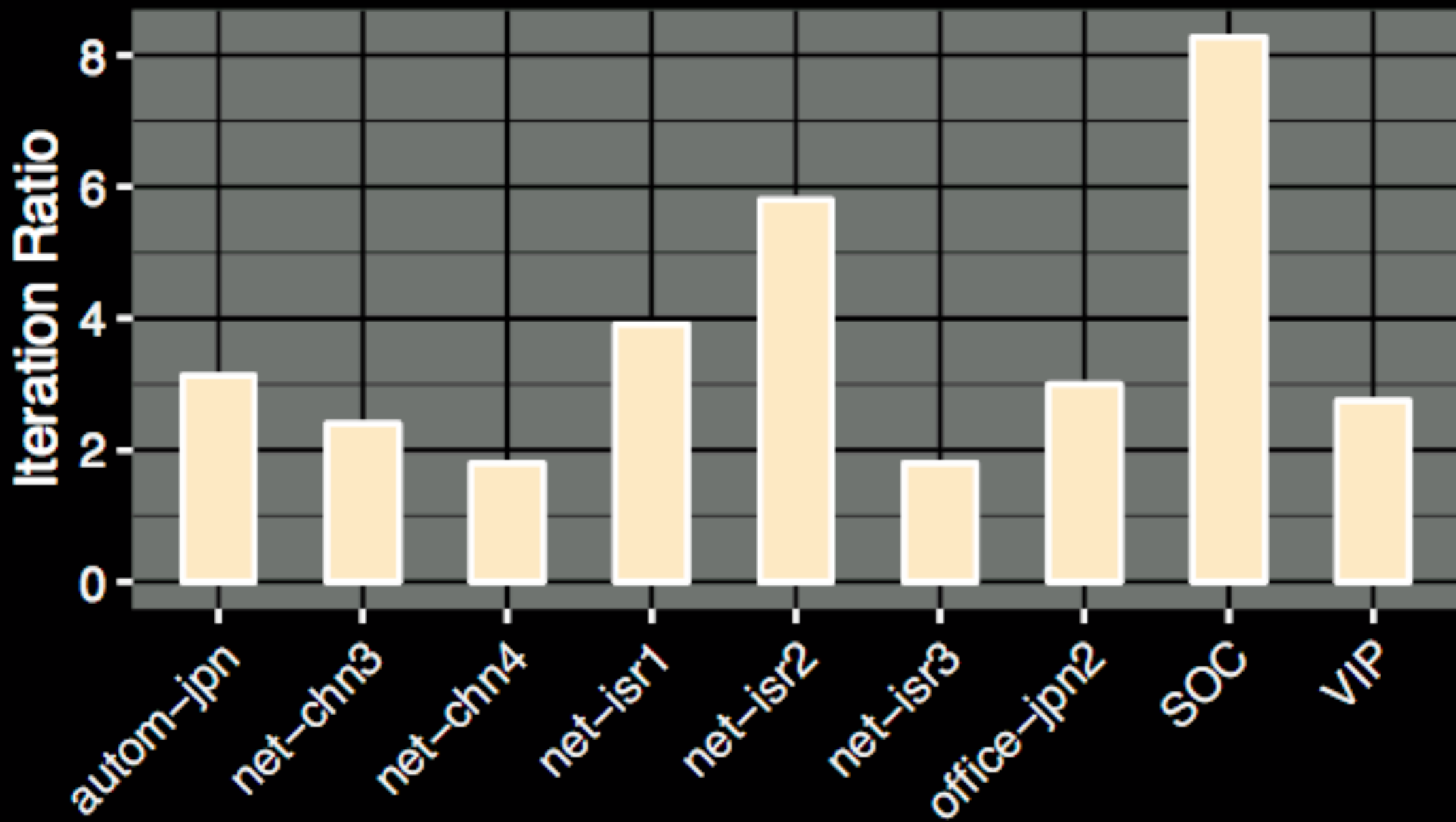


Classifier method doesn't matter

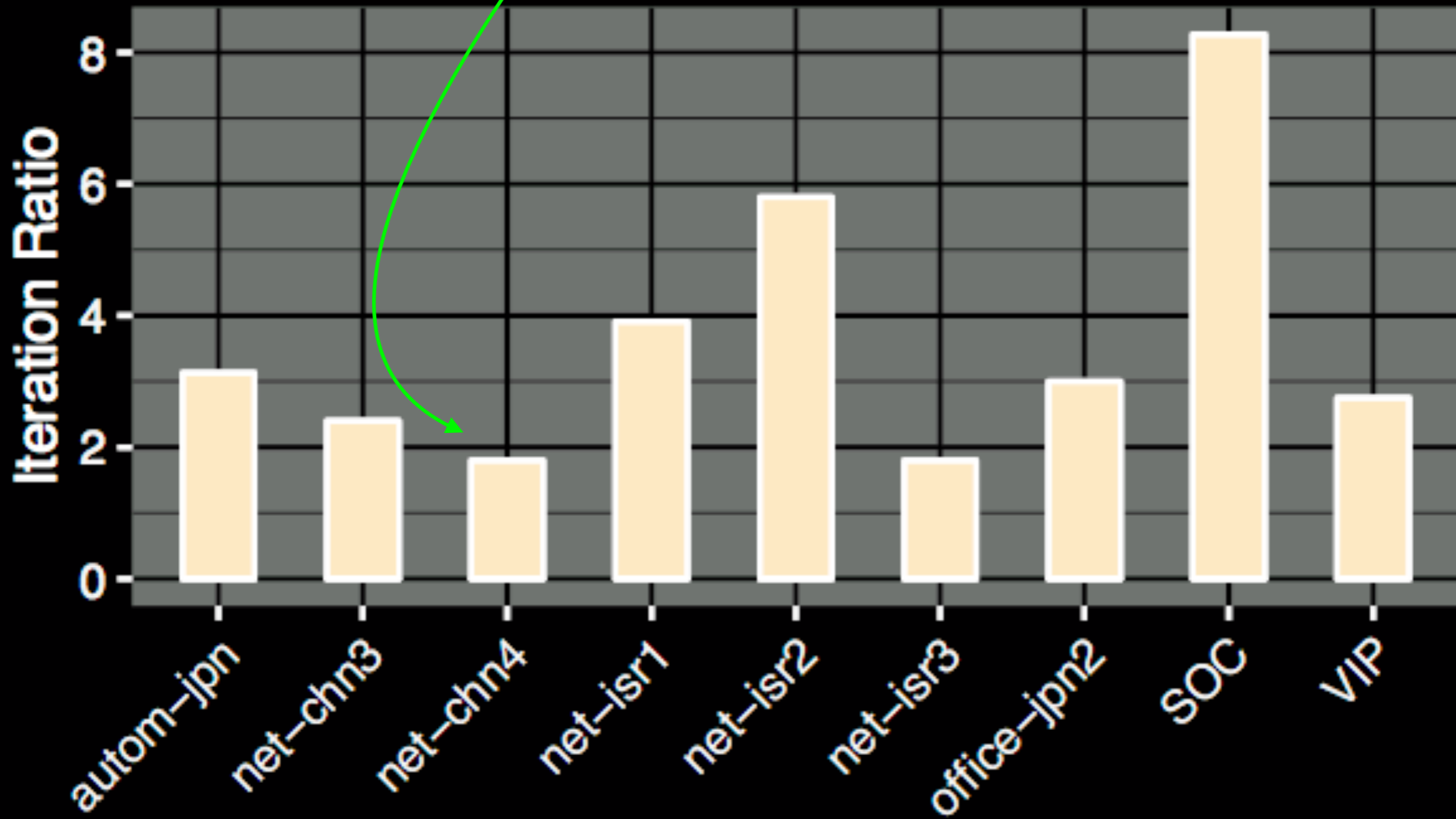


Baseline FCCM 2016 result

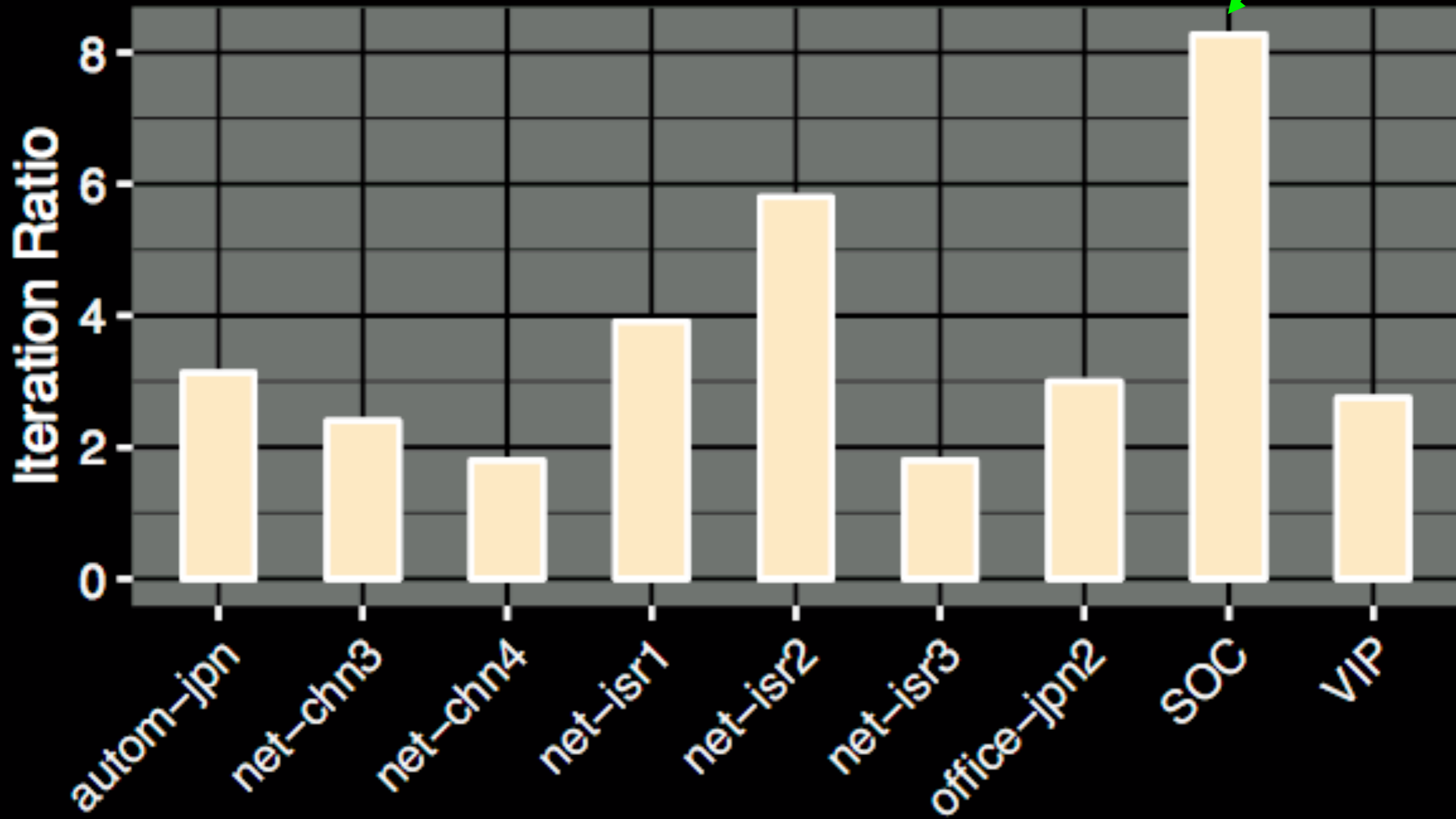




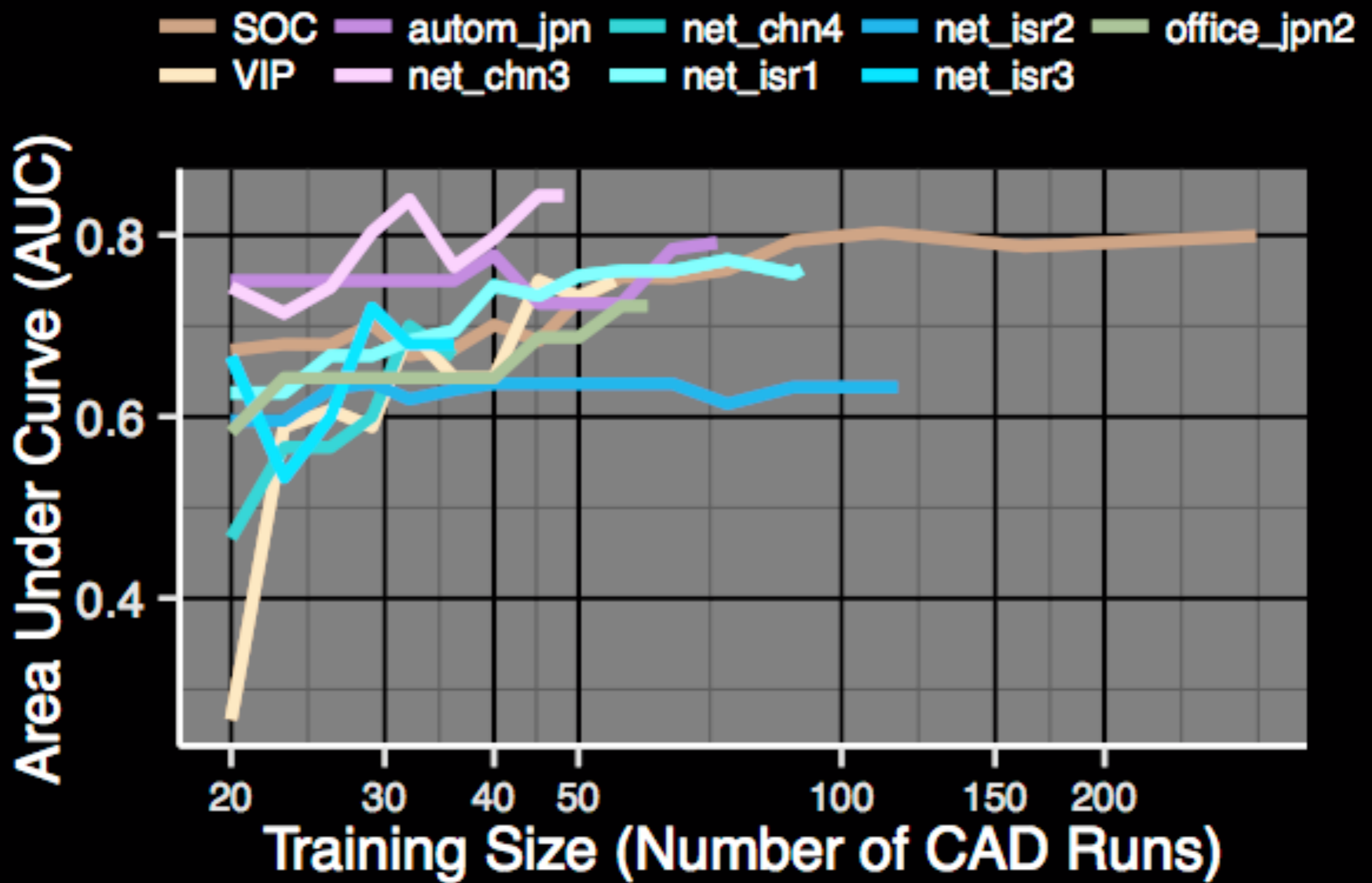
2-3x reduction in parallel FPGA CAD runs



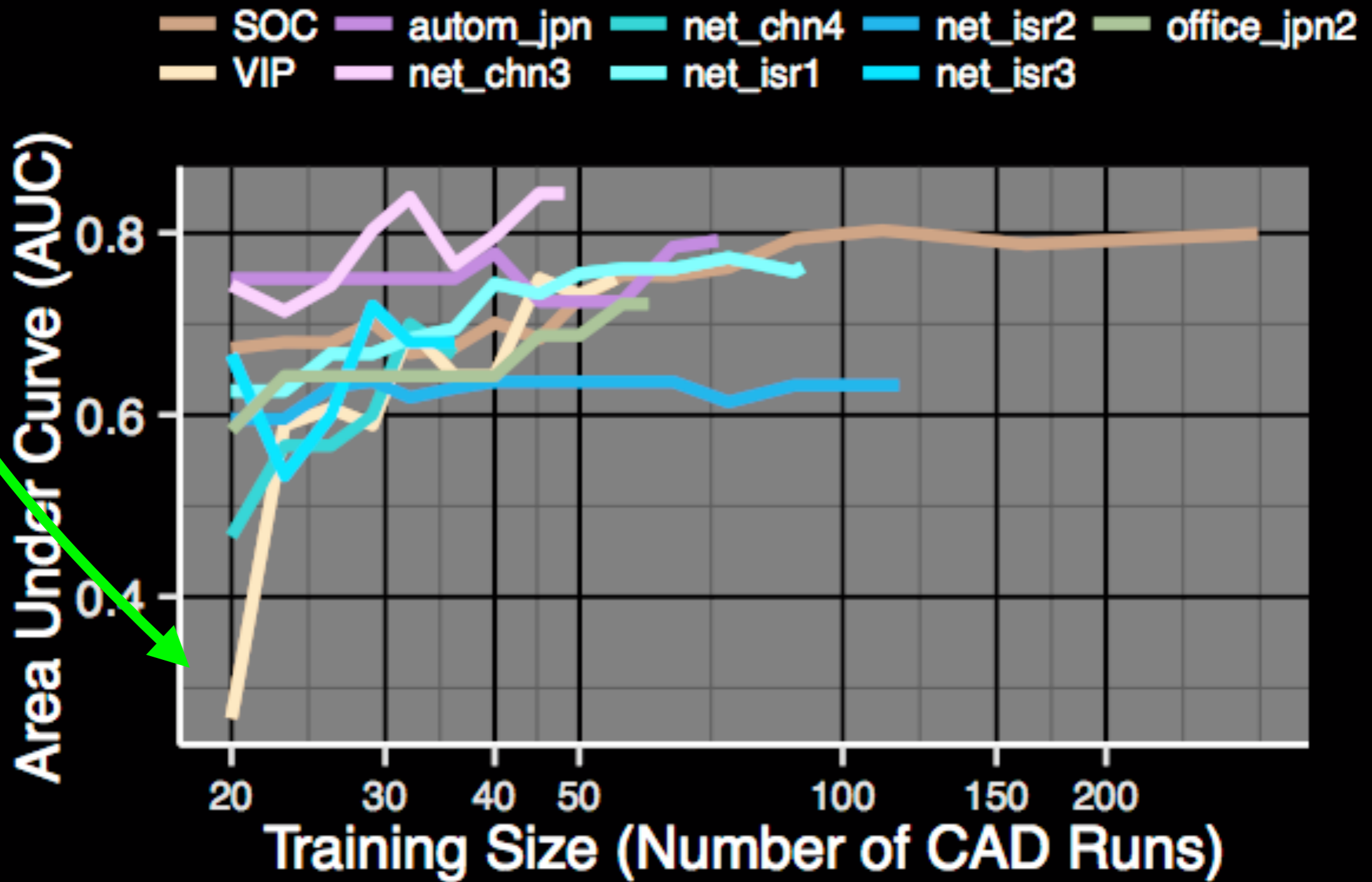
Outlier — fails to meet timing and quits



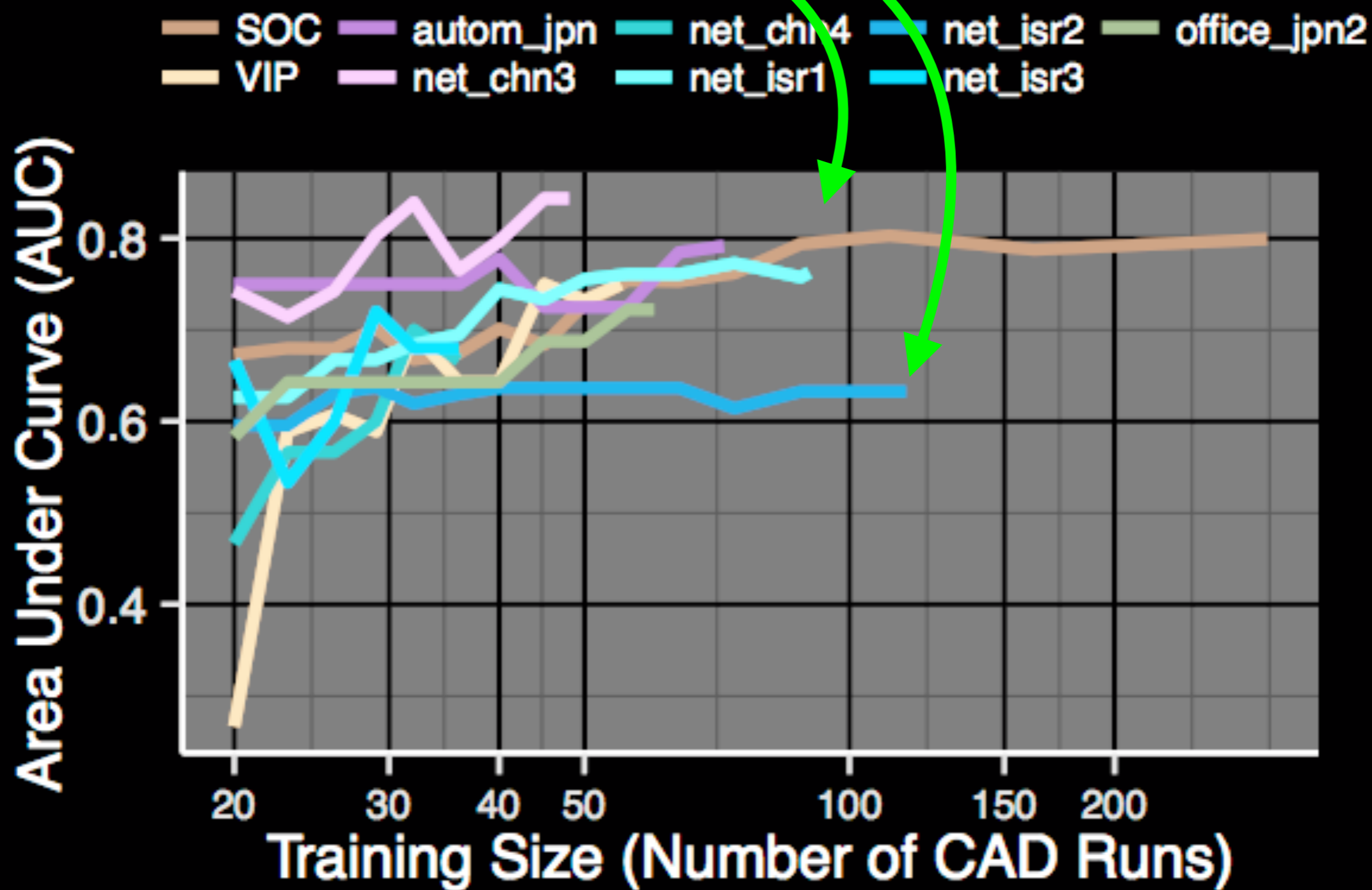
How long does it take
to learn?



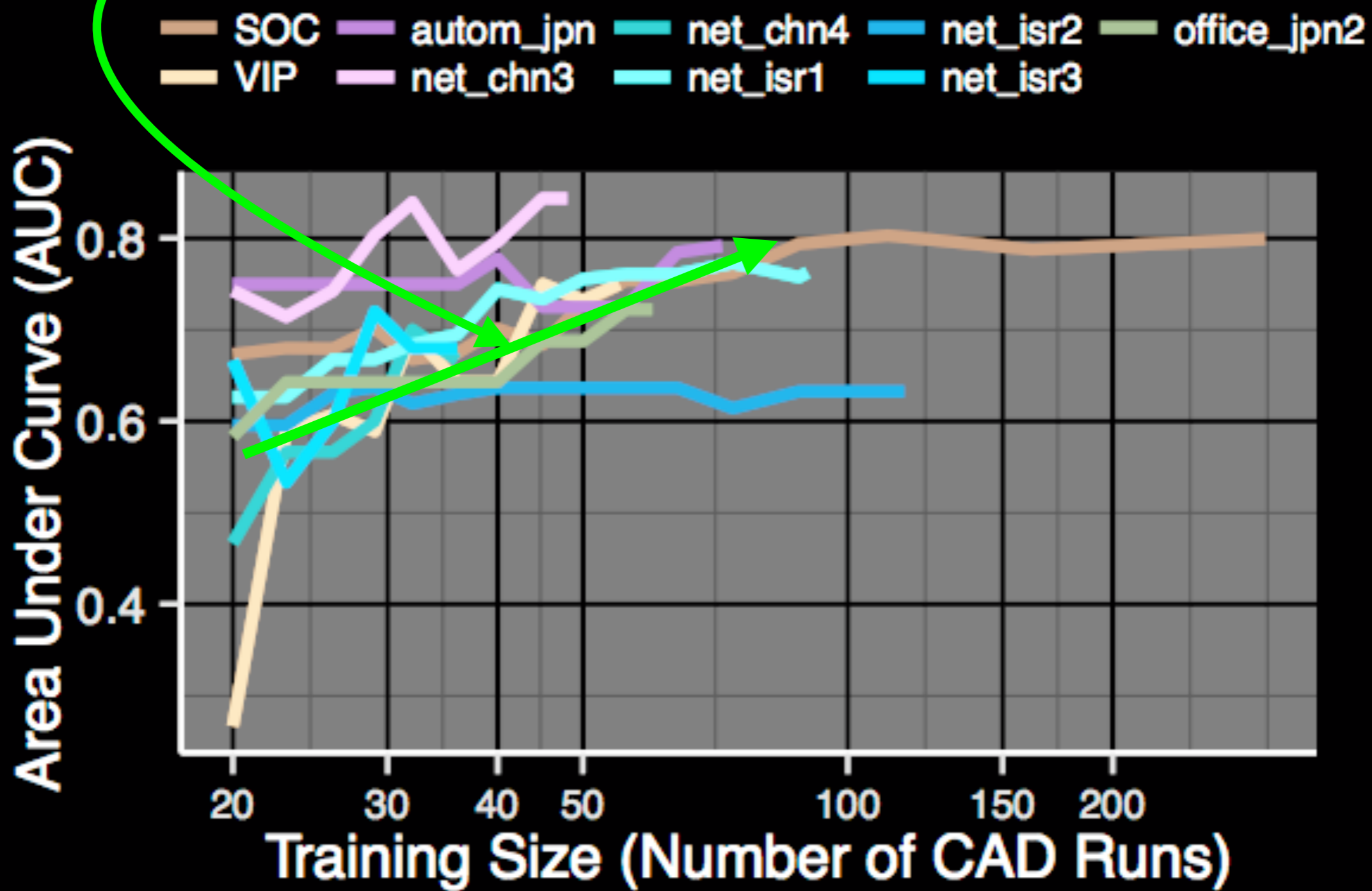
Need at least 20 runs



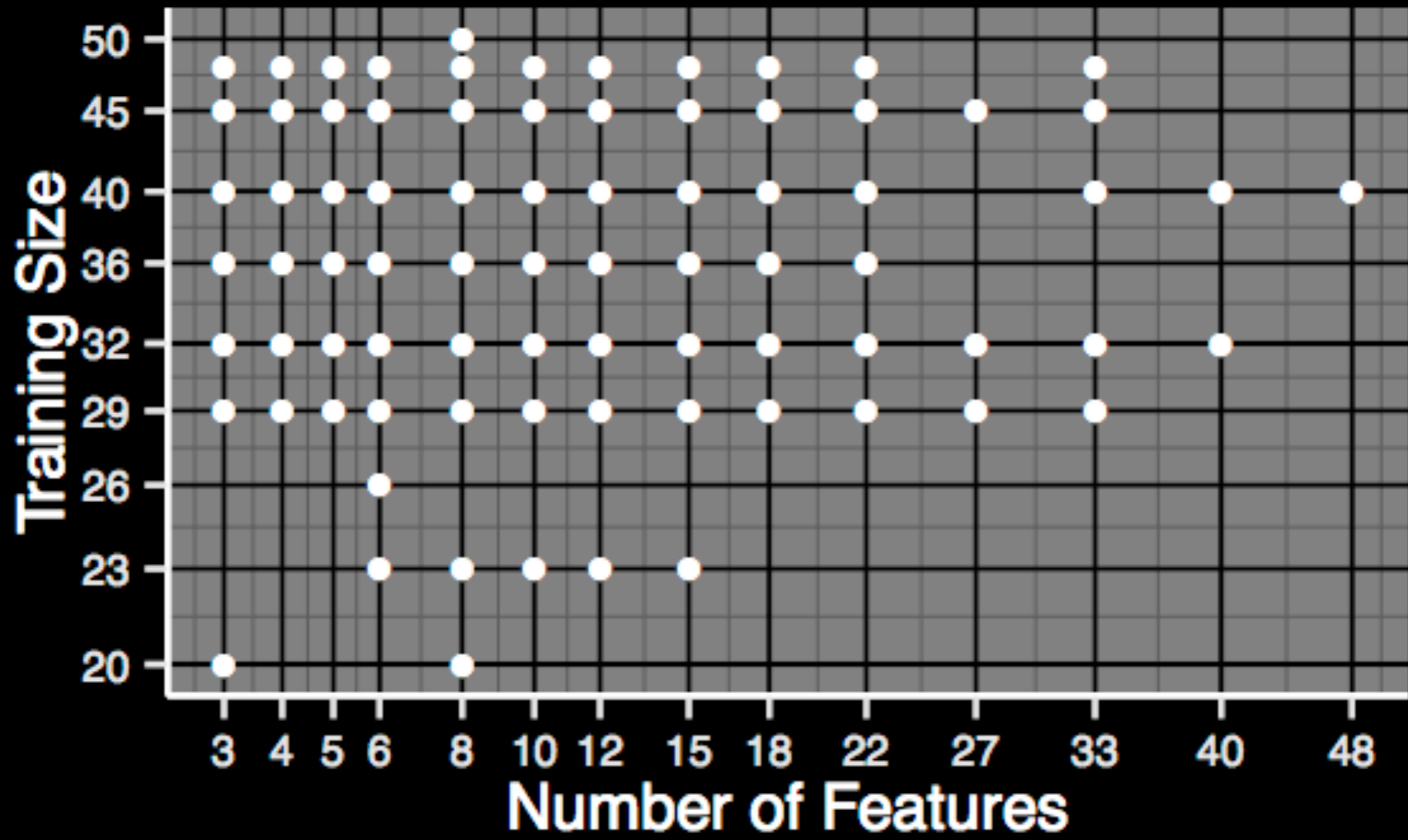
Need 3 rounds x 30 runs configuration

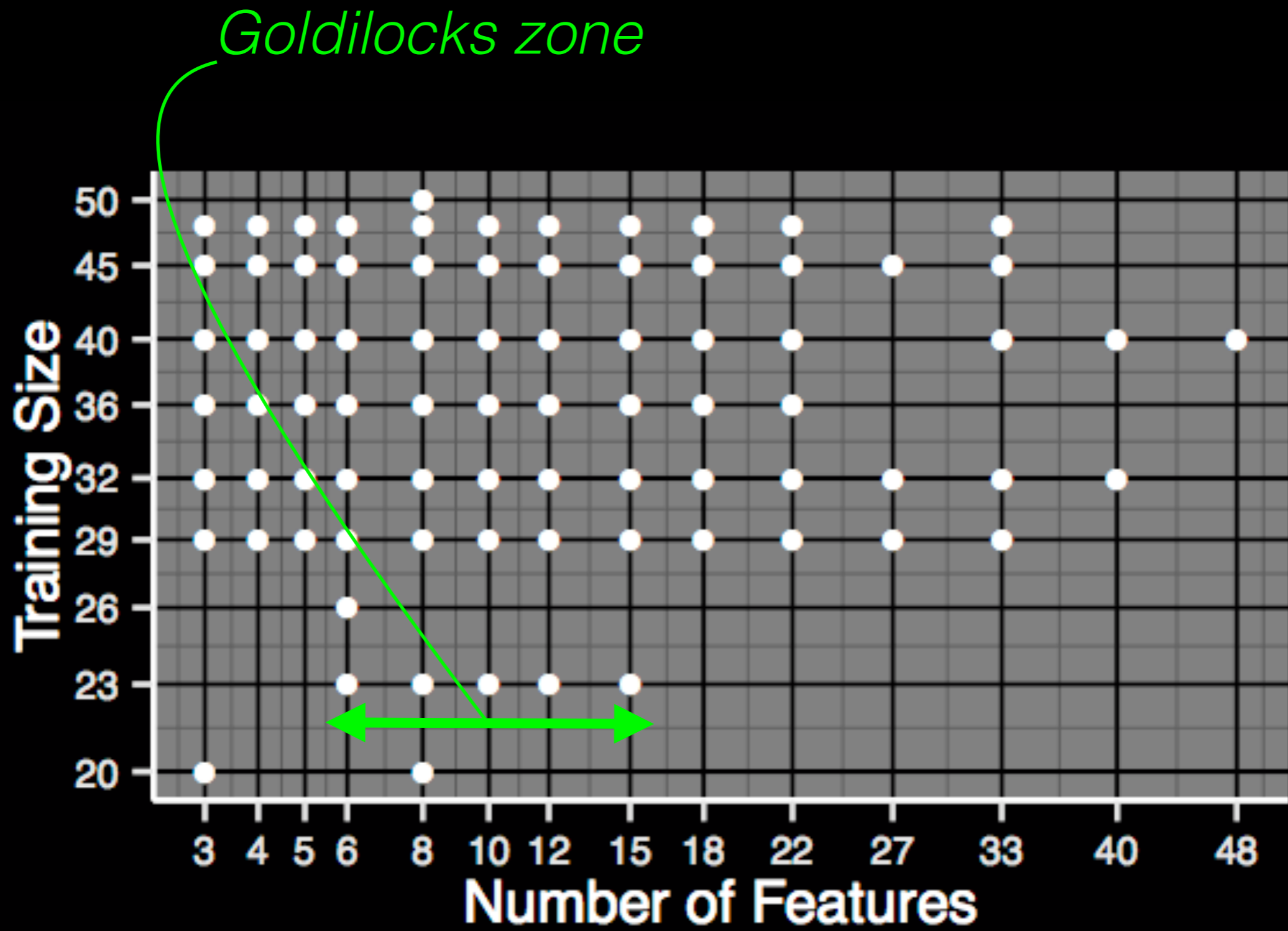


Better AUC the more we run

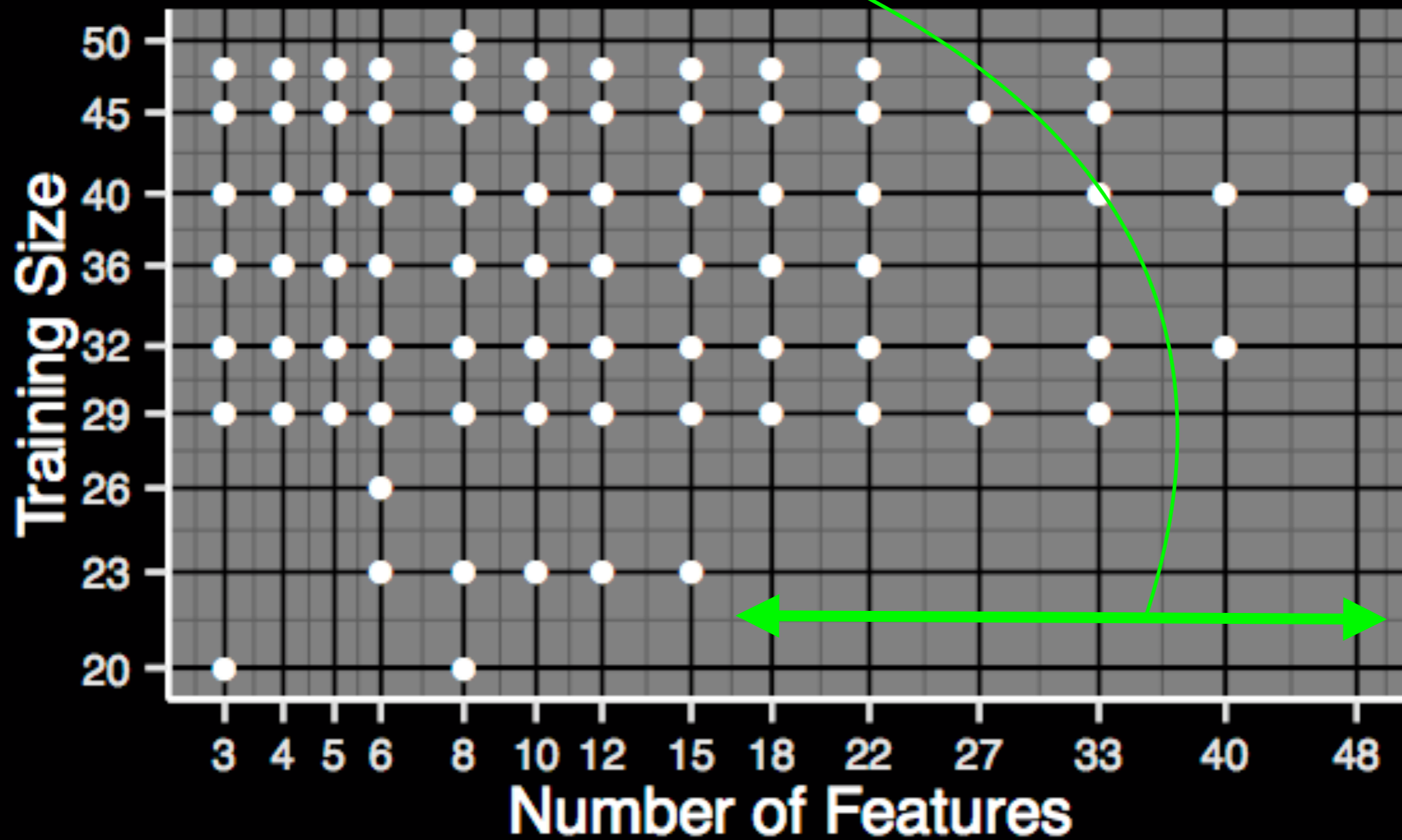


How do we choose the
correct subset of features

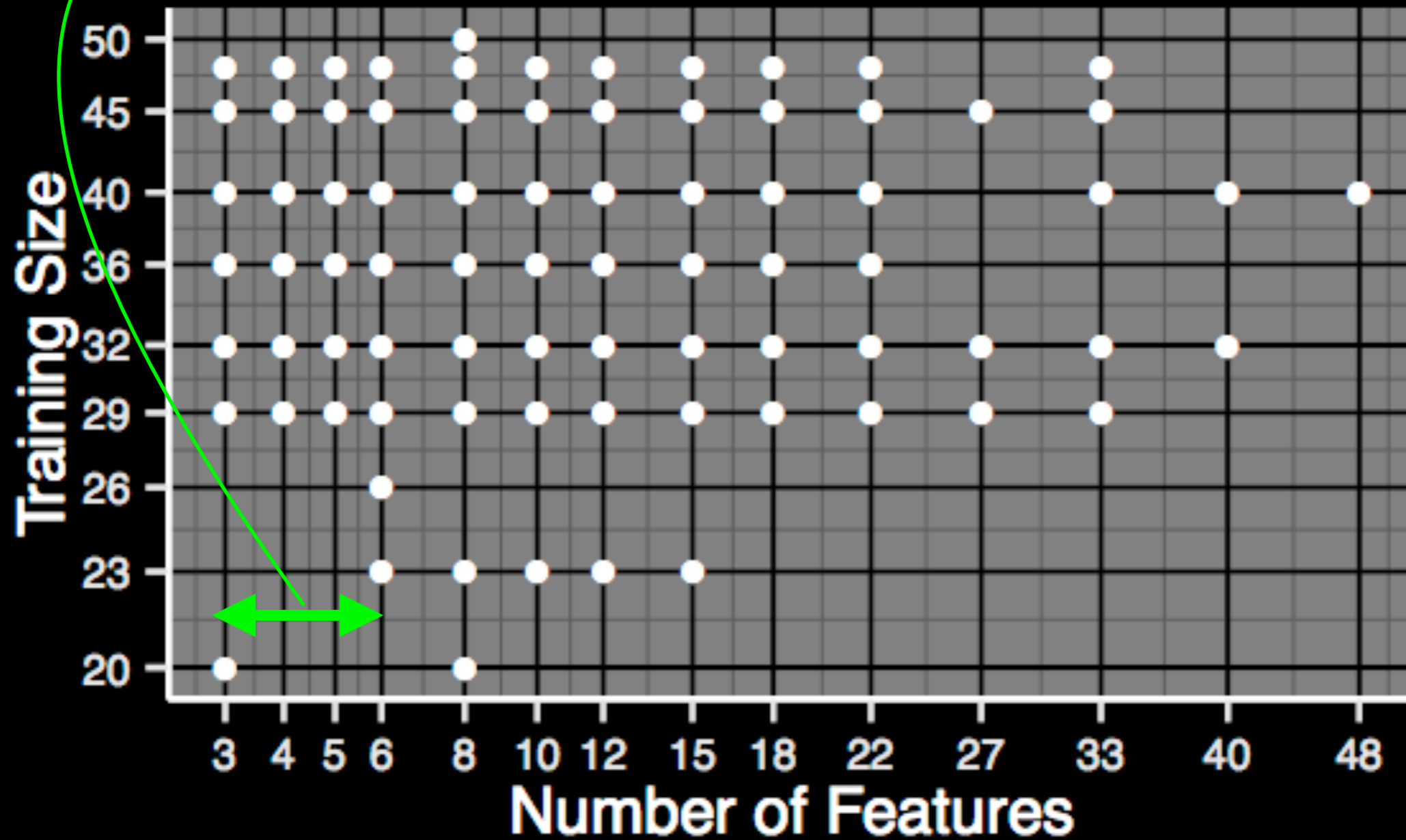




Too many features — large training set



Too few features — more data required for other features



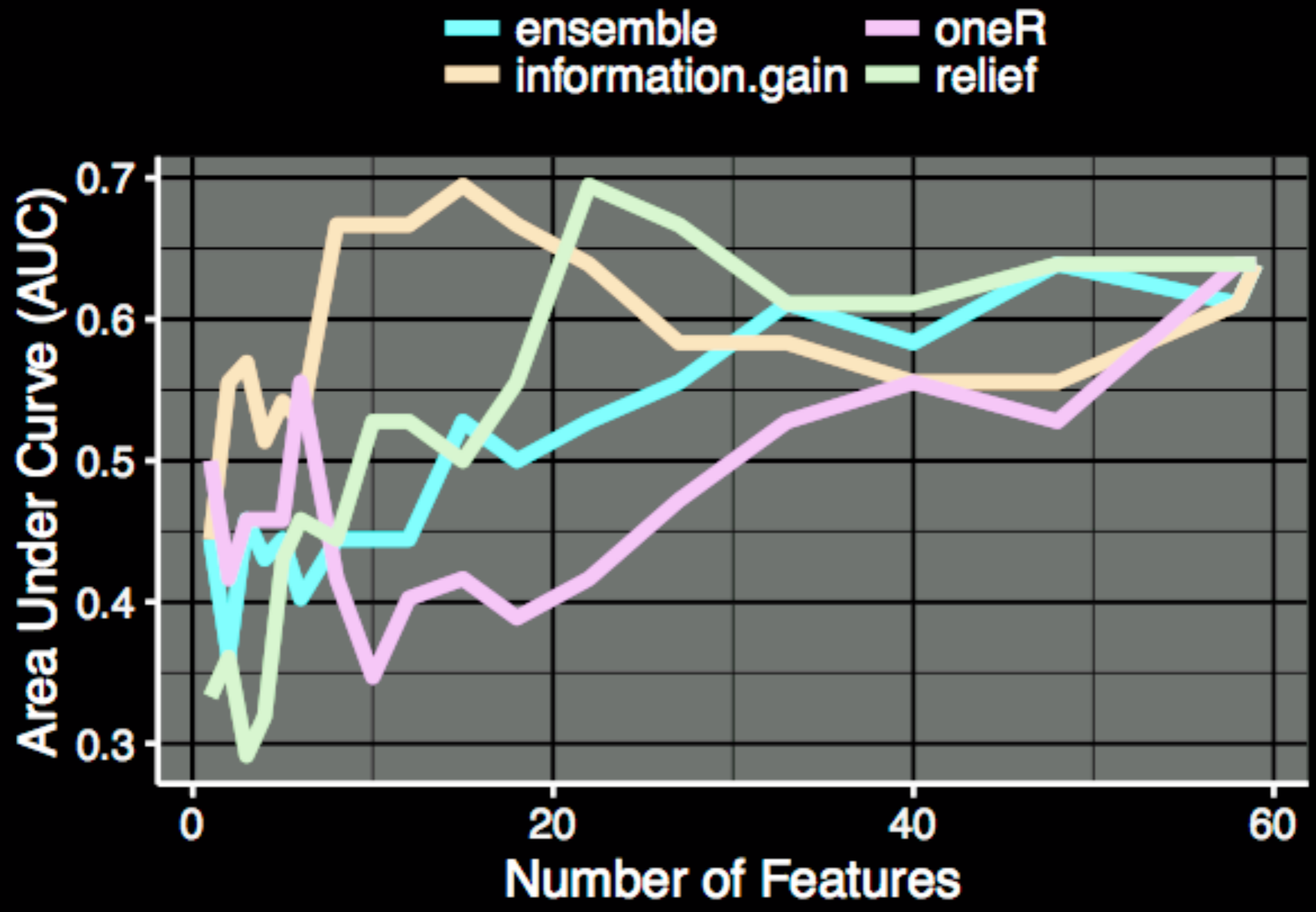
Conclusions

- Feature Selection helps boost AUC of InTime machine learning by ~10%
- Key idea — prune the set of Quartus CAD tool parameters to explore to <22
- Evidence continues to point towards design-specificity

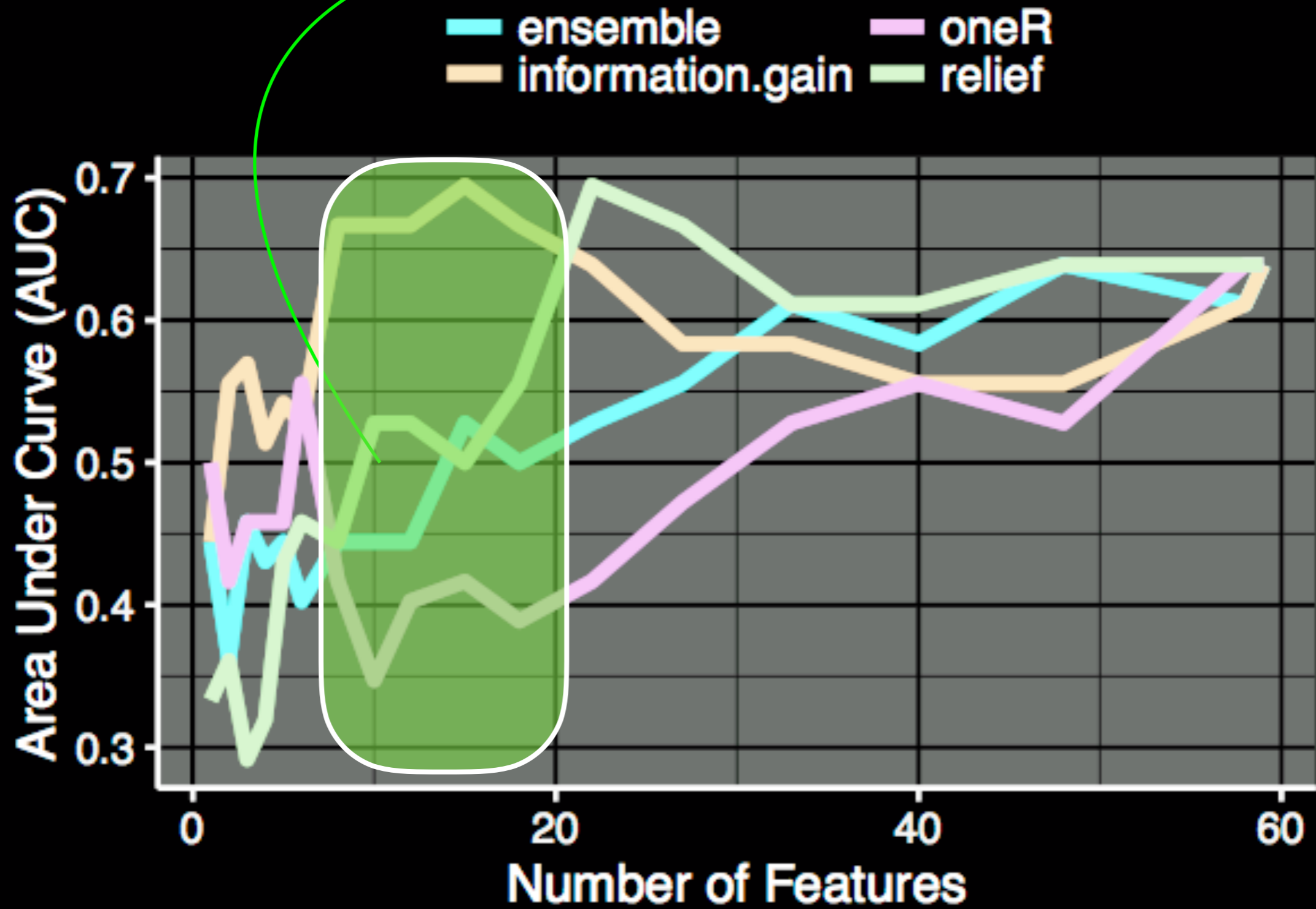
Open-source flow

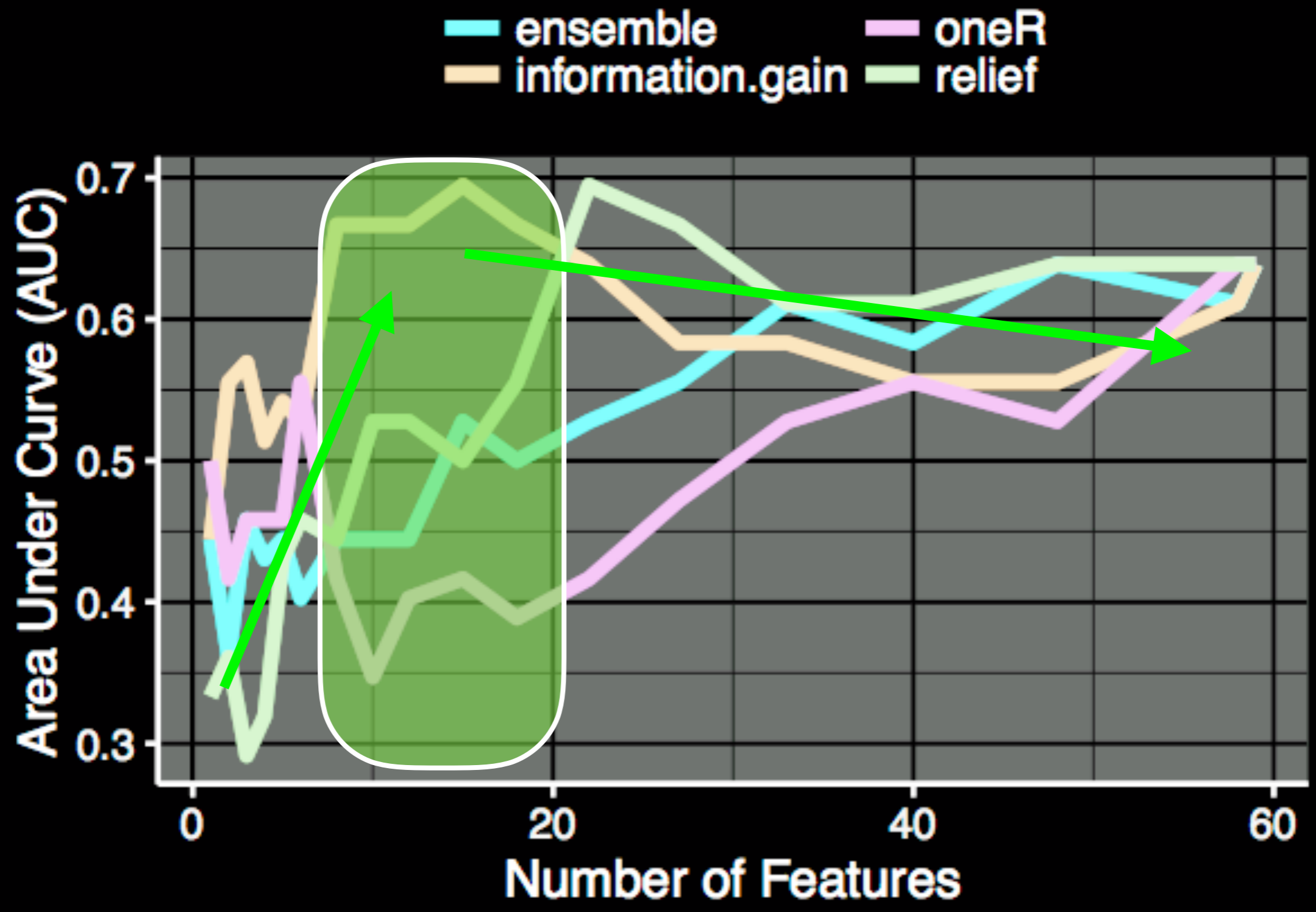
- We are open-sourcing our ML routines
 - <http://bitbucket.org/spinosae/plunify-ml.git>
 - README.md contains instructions for installing and running on your machine
- Requires R (dependencies installed automatically)

Impact of feature count

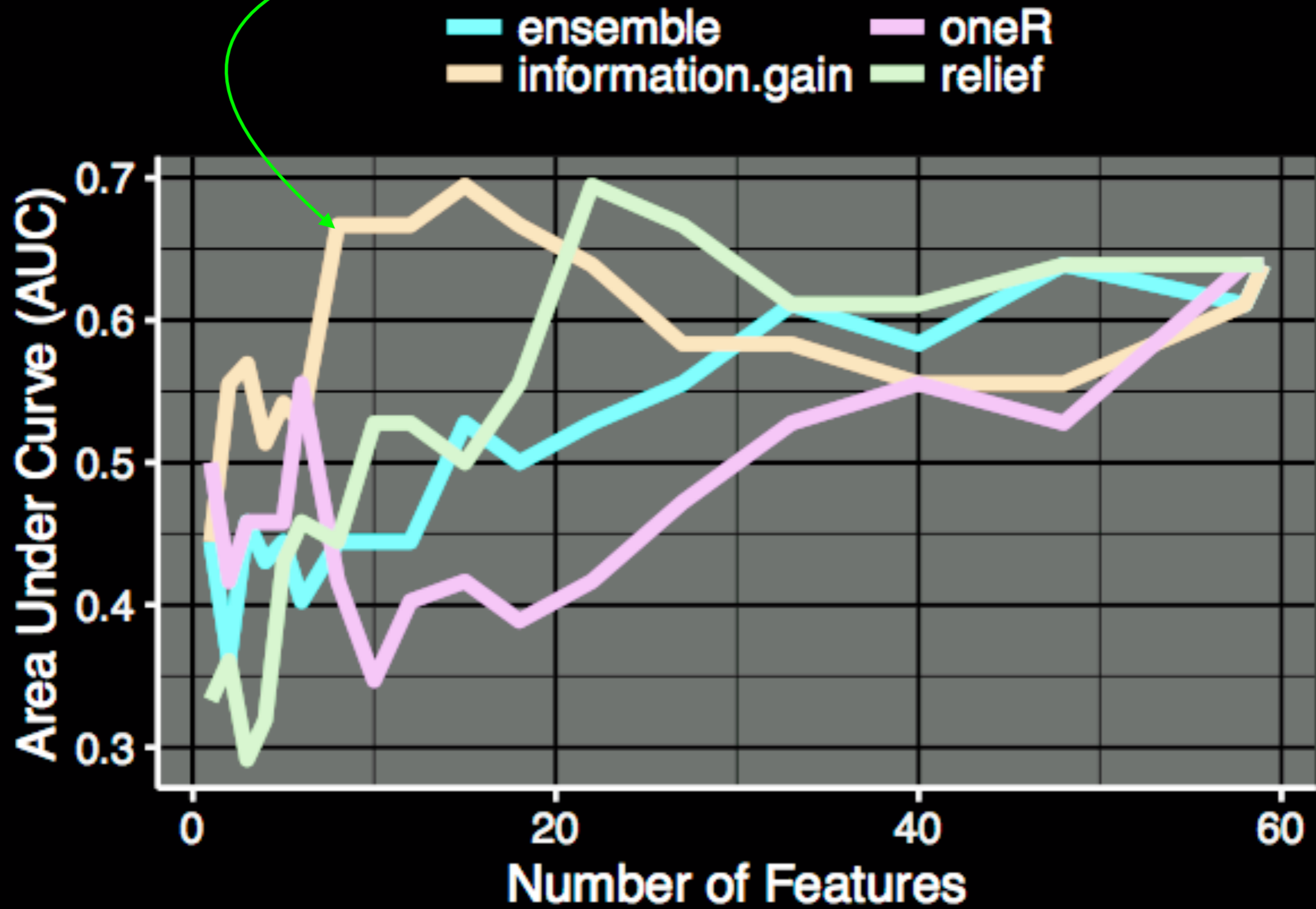


Goldilocks zone





Information.Gain consistently best



training size ● 20 ● 24 ● 28 ● 32 ● 36

